

30V_{DS}/±20V_{GS} N-Channel Shield Trench Power MOSFET

Description

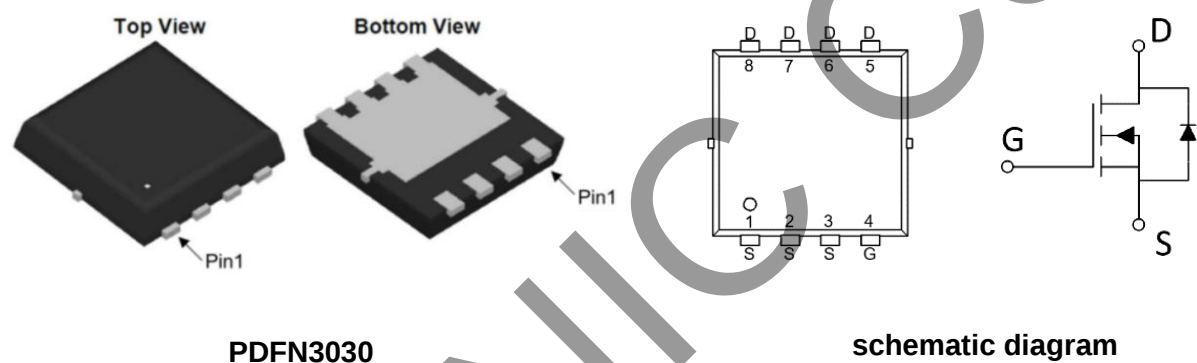
The IP04N03B has the characteristics of extremely low $R_{DS(ON)}$, high-speed switching and high efficiency in DC/DC conversion, marking it suitable for load switching and DC/DC converters.

Application

- Load Switch
- DC/DC conversion
- Portable and Battery-powered Products

General Features

- $V_{DS}=30V, I_D=65A$
- $R_{DS(ON)}=3.1m\Omega$ (TYP.) $V_{GS}=10V, I_D=20A$
- $R_{DS(ON)}=4m\Omega$ (TYP.) $V_{GS}=4.5V, I_D=20A$
- Reliable and Rugged
- Avalanche Rated
- Low On-Resistance
- High Current Capability
- Halogen and Antimony Free, RoHS compliant



Package Marking and Ordering Information

Product ID	Pack		Marking	Qty(PCS)
IP04N03B	PDFN3030	Pb-Free	IP04N03B	5000pcs/Reel

Contents

● Description	1
● Application	1
● General Features	1
● Package Marking And Ordering Information	1
● Absolute Maximum Ratings (Tc=25℃, Unless Otherwise Noted)	3
● Thermal Characteristics	3
● Electrical Characteristics (Tc=25℃, Unless Otherwise Noted)	3
● Dynamic Characteristics	4
● Reverse Diode Characteristics	4
● Typical Characteristics	5
● Package Mechanical Data-PDFN3030	7
● Important Notice	9

Absolute Maximum Ratings ($T_C=25^{\circ}\text{C}$, unless otherwise noted)

Symbol	Parameter	Value	Units
V_{DS}	Drain-Source Voltage ($V_{GS}=0V$)	30	V
V_{GS}	Gate-Source Voltage ($V_{GS}=0V$,static)	± 20	V
I_D	Continuous Drain Current ($T_C=25^{\circ}\text{C}$)	65	A
	Continuous Drain Current ($T_C=100^{\circ}\text{C}$)	45	A
I_{DM}	Pulsed Drain Current	240	A
I_{AS}	Avalanche Current($L=0.01mH$)	126	A
E_{AS}	Single Pulsed Avalanche Energy	80	mJ
P_D	Maximum Power Dissipation ($T_C=25^{\circ}\text{C}$)	35	W
	Maximum Power Dissipation ($T_C=100^{\circ}\text{C}$)	14	W
T_J, T_{STG}	Operating,Storage Temperature Range	$-55\sim 150$	$^{\circ}\text{C}$

Thermal Characteristics

Symbol	Parameter	Min.	Typ.	Max.	Units
$R_{\theta JC}$	Thermal Resistance,Junction-to-Case	-	3.6	-	$^{\circ}\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance,Junction-to-Ambient	-	48	-	$^{\circ}\text{C}/\text{W}$

Electrical Characteristics ($T_C=25^{\circ}\text{C}$, unless otherwise noted)

Symbol	Parameter	Test Condition	Min	Typ	Max	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	30	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=30V, V_{GS}=0V$	-	-	1	μA
I_{GSS}	Gate -Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	± 100	nA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	1.0	1.5	2.0	V
$R_{DS(ON)}$	Drain-Source On-stage Resistance	$V_{GS}=10V, I_D=20A$	-	3.1	4	m Ω
		$V_{GS}=4.5V, I_D=20A$	-	4	5.2	

Dynamic Characteristics

Symbol	Parameter	Test Condition	Min	Typ	Max	Unit
C_{iss}	Input capacitance	$V_{DS}=15V$ $V_{GS}=0V$ $f=1MHz$	-	1147	-	pF
C_{oss}	Output capacitance		-	562	-	
C_{rss}	Reverse transfer capacitance		-	77	-	
R_g	Gate Resistance	$f=1MHz$	-	1.27	-	Ω
Q_g	Total Gate Charge	$V_{DS}=15V$ $V_{GS}=10V$ $I_D=20A$	-	21.3	-	nC
Q_{gs}	Gate Source Charge		-	4.5	-	
Q_{gd}	Gate Drain Charge		-	4	-	
$t_{d(on)}$	Turn-on delay Time	$V_{GS}=10V$ $V_{DS}=15V$ $R_L=0.75\Omega$ $R_G=3\Omega$	-	4.8	-	ns
t_r	Rise time		-	28.8	-	
$t_{d(off)}$	Turn-off delay Time		-	23.2	-	
t_f	Fall time		-	8	-	

Reverse Diode Characteristics

Symbol	Parameter	Test Condition	Min	Typ	Max	Unit
V_{SD}	Body Diode Forward Voltage	$V_{GS}=0V, I_{SD}=1A$	-	0.7	-	V
t_{rr}	Reverse Recovery Time	$V_{GS}=0V, I_{SD}=20A$ $dI/dt=100A/\mu s$	-	14	-	ns
Q_{rr}	Reverse Recovery Charge		-	1.6	-	nC

Notes :

- (1) The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper
- (2) The data tested by pulsed , pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
- (3) The power dissipation is limited by 150°C junction temperature
- (4) The data is theoretically the same as ID and IDM , in real applications , should be limited by total power dissipation

Typical Characteristics

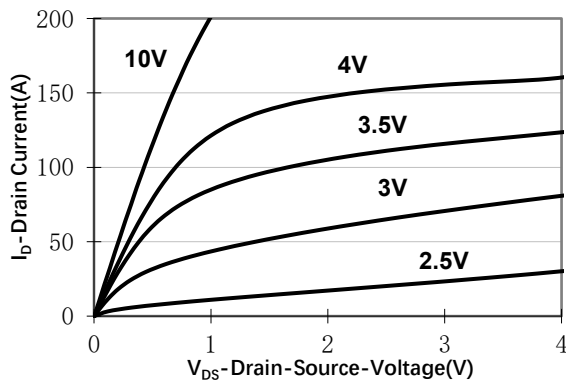


Figure 1. On-Region Characteristics

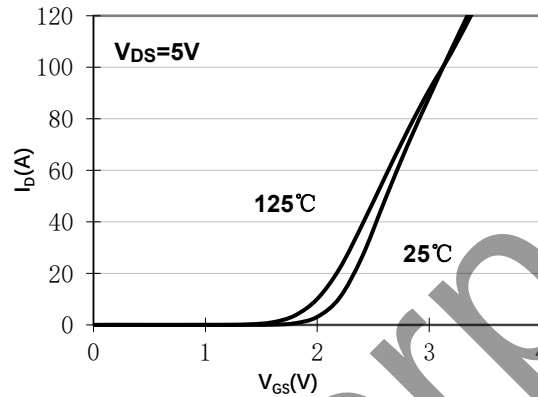


Figure 2. Transfer Characteristics

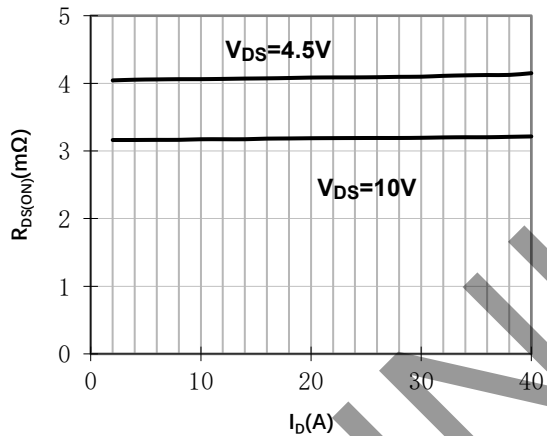


Figure 3. On-Resistance vs. Drain Current and Gate Voltage

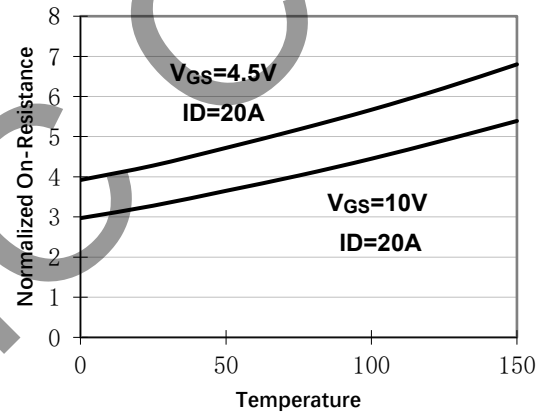


Figure 4. On-Resistance vs. Junction Temperature

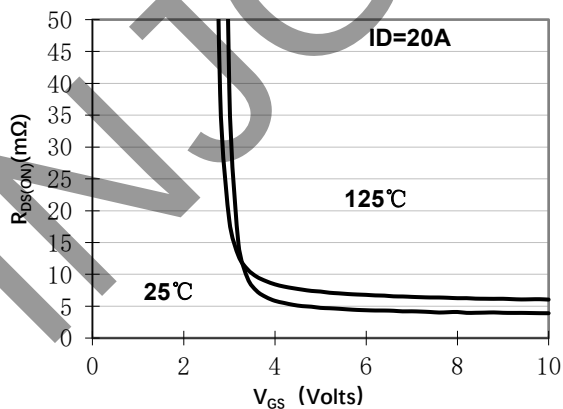


Figure 5. On-Resistance vs. Gate-Source Voltage

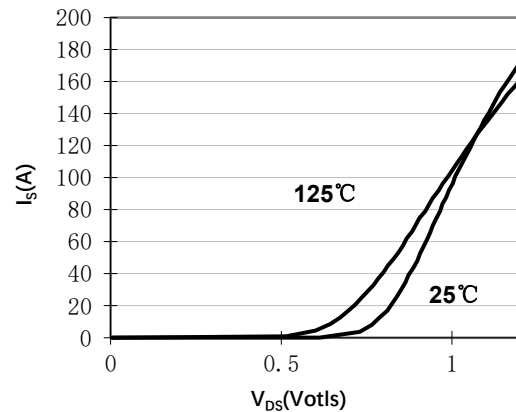


Figure 6. Body-Diode Characteristics

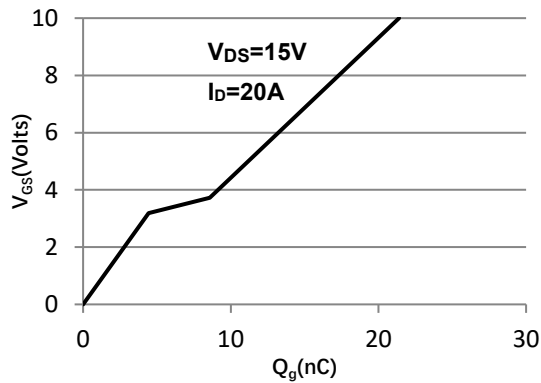


Figure 7. Gate-Charge Characteristics

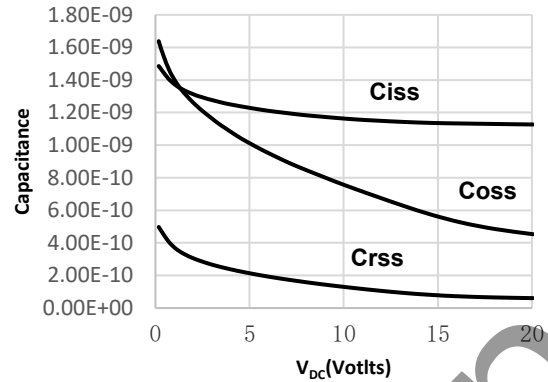


Figure 8. Capacitance Characteristics

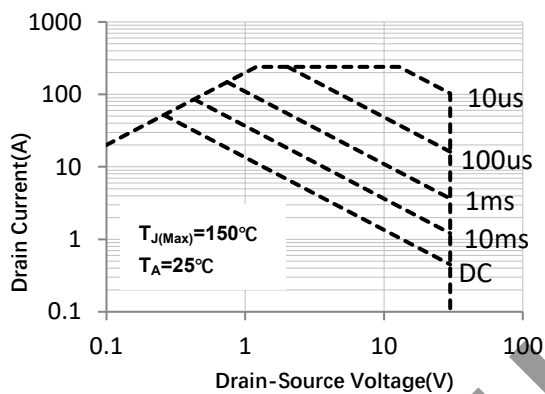


Figure 9. Maximum Forward Biased Safe Operating Area

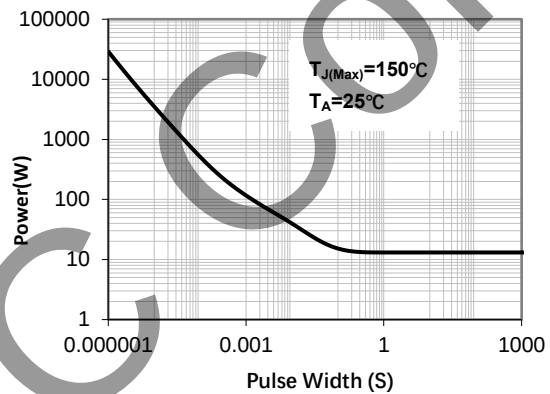


Figure 10. Single Pulse Power Rating Junction-to-Ambient

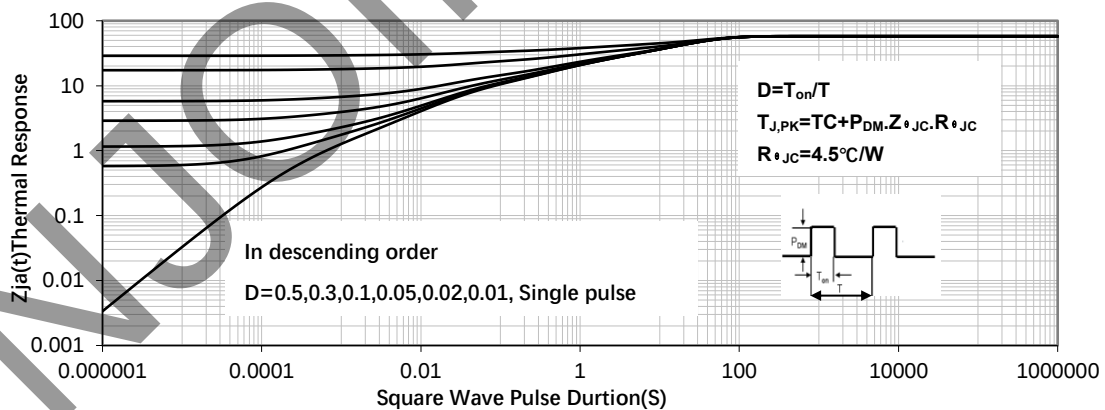
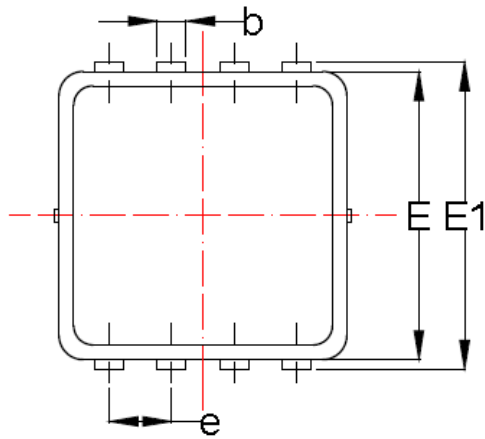
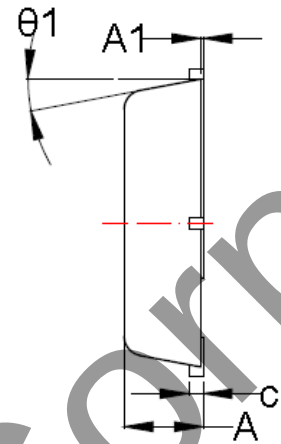


Figure 11. Normalized Maximum Transient Thermal Impedance

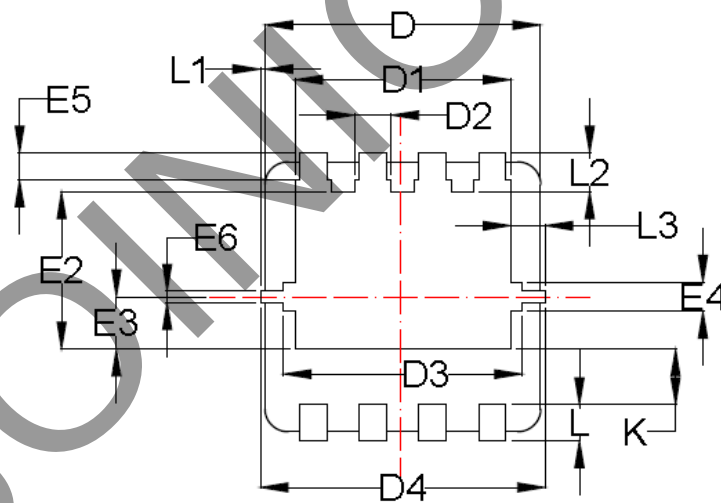
Package Mechanical Data-PDFN3030



TOP VIEW



SIDE VIEW



BOTTOM VIEW

Symbol	Dimensions In Millimeters		
	Min	NOM	MAN
A	0.65	0.775	0.90
A1	0.00	0.025	0.05
b	0.20	0.30	0.40
c	0.10	0.15	0.25
D	2.90	3.00	3.20
D1	2.25	2.35	2.69
D2	0.30	0.40	0.50
D3	2.50	2.60	2.70
D4	3.00	3.10	3.20
E	2.90	3.00	3.10
E1	3.00	3.20	3.60
E2	1.35	1.75	1.85
E3	0.48	0.58	0.68
E4	0.23	0.33	0.43
E5	0.20	0.30	0.40
E6	0.075	0.125	0.175
e	0.60	0.65	0.70
K	0.52	0.62	0.72
L	0.15	0.33	0.50
L1	0	0.10	0.20
L2	0.15	0.40	0.65
L3	0.275	0.375	0.475
Θ1	0°	10°	14°

Important Notice

INJOINIC TECHNOLOGY Corp. and its subsidiaries reserve the right to make corrections, enhancements, improvements and other changes to its semiconductor products and services. Buyers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All semiconductor products (also referred to herein as “components”) are sold subject to INJOINIC TECHNOLOGY Corp.'s terms and conditions of sale supplied at the time of order acknowledgment.

INJOINIC TECHNOLOGY Corp. assumes no liability for applications assistance or the design of Buyers' products. Buyers are responsible for their products and applications using INJOINIC's components. To minimize the risks associated with Buyers' products and applications, Buyers should provide adequate design and operating safeguards.

Buyer acknowledges and agrees that it is solely responsible for compliance with all legal, regulatory and safety-related requirements concerning its products, and any use of INJOINIC's components in its applications, notwithstanding any applications-related information or support that may be provided by INJOINIC. Buyer represents and agrees that it has all the necessary expertise to create and implement safeguards which anticipate dangerous consequences of failures, monitor failures and their consequences, lessen the likelihood of failures that might cause harm and take appropriate remedial actions. Buyer will fully indemnify INJOINIC TECHNOLOGY and its representatives against any damages arising out of the use of any INJOINIC's components in safety-critical applications.

Reproduction of significant portions of INJOINIC's information in INJOINIC's data books or data sheets is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. INJOINIC TECHNOLOGY Corp. is not responsible or liable for such altered documentation. Copying information from third parties may be subject to additional restrictions.

INJOINIC will update this document from time to time. The actual parameters of the product may vary due to different models or other items. This document is not a warranty of any kind, express or implied.

Resale of INJOINIC's components or services with statements different from or beyond the parameters stated by INJOINIC for that component or service voids all express and any implied warranties for the associated INJOINIC's component or service and is an unfair and deceptive business practice. INJOINIC TECHNOLOGY Corp. is not responsible or liable for any such statements.