

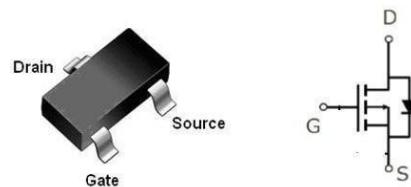
Features

- Low $R_{DS(on)}$ @ $V_{GS}=-10V$
- -5V Logic Level Control
- P Channel SOT23-3 Package
- Pb-Free, RoHS Compliant

$V_{(BR)DSS}$	$R_{DS(ON)}\text{ Typ}$	$I_D \text{ Max}$
-30V	49mΩ @ -10V	-4.2A
	54mΩ @ -4.5V	

Applications

- Load Switch
- Switching circuits
- High-speed line driver
- Power Management Functions



SOT23-3

Order Information

Product	Package	Marking	Packing	Min Unit Quantity
AO3401B	SOT23-3	X14A	3000PCS/Reel	3000PCS

Absolute Maximum Ratings

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Symbol	Parameter	Rating	Unit	
Common Ratings (TA=25°C Unless Otherwise Noted)				
V_{GS}	Gate-Source Voltage	±12	V	
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	-30	V	
T_J	Maximum Junction Temperature	150	°C	
T_{STG}	Storage Temperature Range	-50 to 150	°C	
Mounted on Large Heat Sink				
I_{DM}	Pulse Drain Current Tested①	TA = 25°C	-16	A
I_D	Continuous Drain Current	TA = 25°C	-4.2	A
		TA = 70°C	-3.2	
P_D	Maximum Power Dissipation	TA = 25°C	1.2	W
		TA = 70°C	0.9	
R_{JA}	Thermal Resistance Junction-Ambient	80	°C/W	

Symbol	Parameter	Condition	Min	Typ	Max	Unit
Static Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise stated)						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ $I_D=-250\mu\text{A}$	-30	--	--	V
I_{DSS}	Zero Gate Voltage Drain Current($T_A=25^\circ\text{C}$)	$V_{\text{DS}}=-30\text{V}$, $V_{\text{GS}}=0\text{V}$	--	--	-1	μA
	Zero Gate Voltage Drain Current($T_A=125^\circ\text{C}$)	$V_{\text{DS}}=-24\text{V}$, $V_{\text{GS}}=0\text{V}$	--	--	-100	μA
I_{GSS}	Gate-Body Leakage Current	$V_{\text{GS}}=\pm 16\text{V}$, $V_{\text{DS}}=0\text{V}$	--	--	± 100	nA
$V_{\text{GS}(\text{TH})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$, $I_D=-250\mu\text{A}$	-0.5	-0.8	-1.2	V
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance ^②	$V_{\text{GS}}=-10\text{V}$, $I_D=-4\text{A}$	--	49	54	$\text{m}\Omega$
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance ^②	$V_{\text{GS}}=-4.5\text{V}$, $I_D=-3\text{A}$	--	54	69	$\text{m}\Omega$
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance ^②	$V_{\text{GS}}=-3.3\text{V}$, $I_D=-3\text{A}$	--	58	80	$\text{m}\Omega$
Dynamic Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise stated)						
C_{iss}	Input Capacitance	$V_{\text{DS}}=-15\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	--	655	--	pF
C_{oss}	Output Capacitance		--	65	--	pF
C_{rss}	Reverse Transfer Capacitance		--	53	--	pF
Q_g	Total Gate Charge	$V_{\text{DS}}=-15\text{V}$ $I_D=-4\text{A}$, $V_{\text{GS}}=-4.5\text{V}$	--	7.2	--	nC
Q_{gs}	Gate Source Charge		--	1.5	--	nC
Q_{gd}	Gate Drain Charge		--	2.6	--	nC
Switching Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise stated)						
$t_{\text{d(on)}}$	Turn on Delay Time	$V_{\text{DD}}=-15\text{V}$, $I_D=-2\text{A}$, $R_G=3.3\Omega$, $V_{\text{GS}}=-10\text{V}$	--	7	--	ns
t_r	Turn on Rise Time		--	3.8	--	ns
$t_{\text{d(off)}}$	Turn Off Delay Time		-	35	--	ns
t_f	Turn Off Fall Time		--	10.5	--	ns
Source Drain Diode Characteristics						
I_{SD}	Source drain current(Body Diode)	$T_A=25^\circ\text{C}$	--	--	-2	A
V_{SD}	Forward on voltage ^②	$T_J=25^\circ\text{C}$, $I_{\text{SD}}=-4\text{A}$, $V_{\text{GS}}=0\text{V}$	--	-0.88	-1.2	V

Notes:

① Pulse width limited by maximum allowable junction temperature

②Pulse test ; Pulse width 300 s, duty cycle 2%.

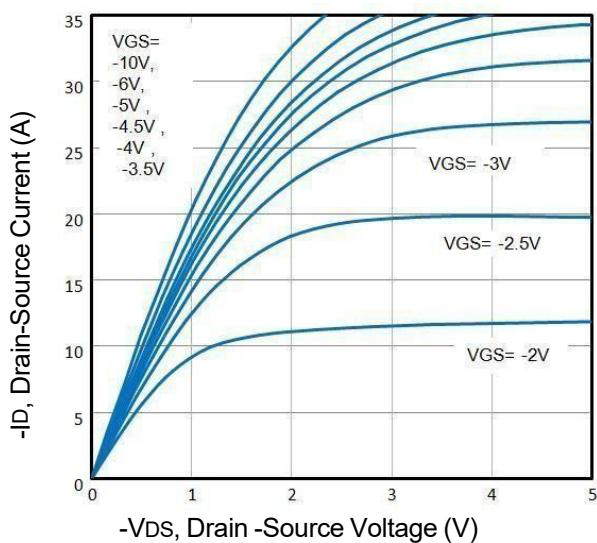


Fig1. Typical Output Characteristics

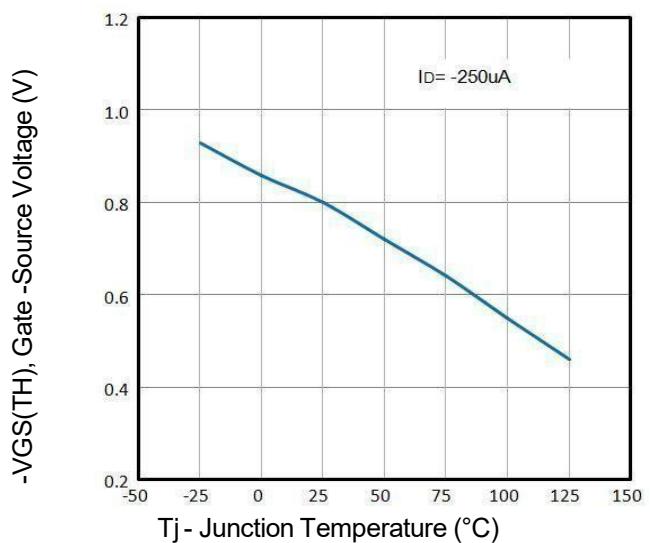


Fig2. Normalized Threshold Voltage Vs. Temperature

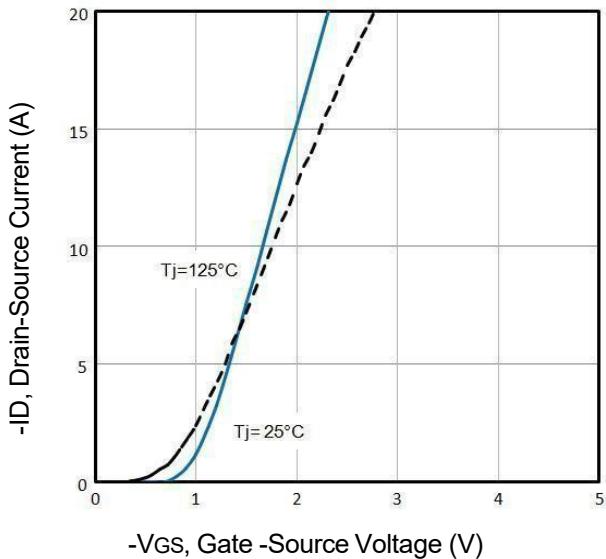


Fig3. Typical Transfer Characteristics

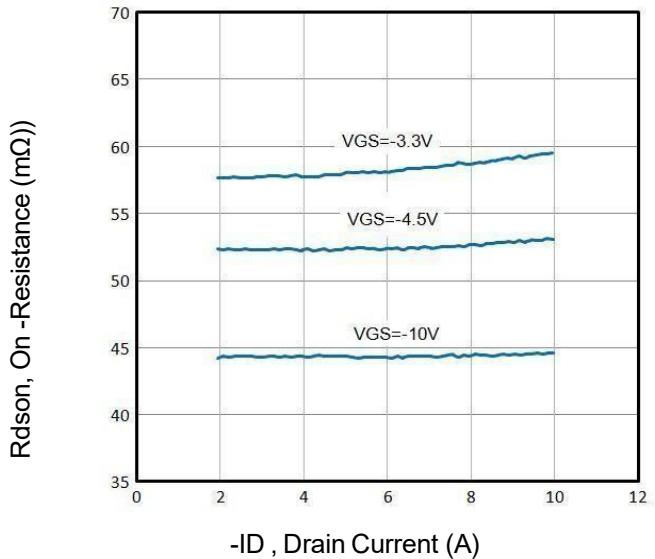


Fig4. On-Resistance vs. Drain Current and Gate

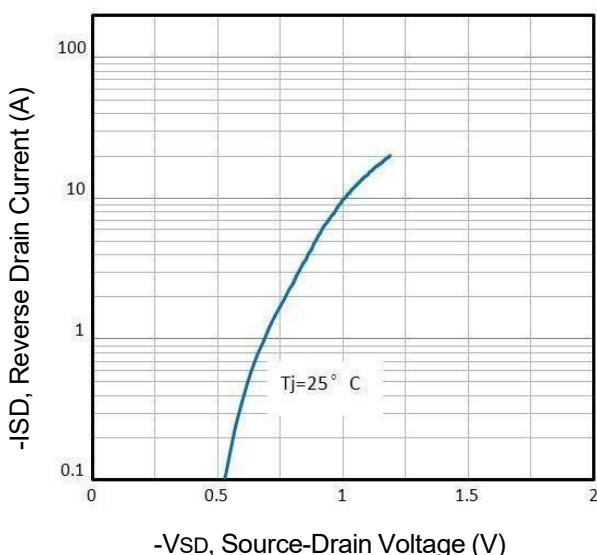


Fig5. Typical Source-Drain Diode Forward Voltage

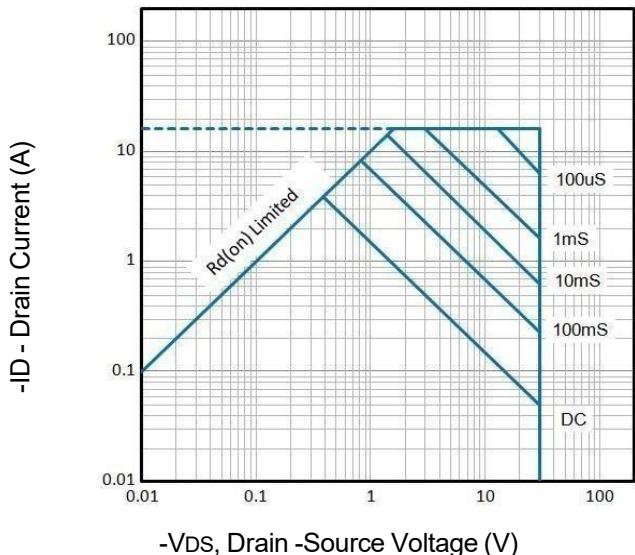


Fig6. Maximum Safe Operating Area

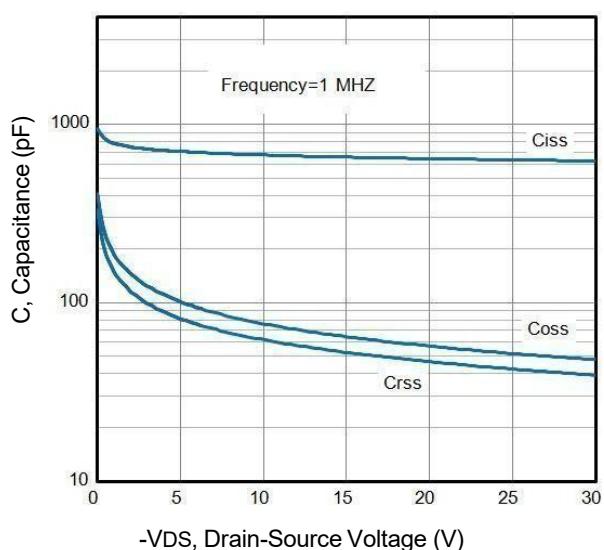


Fig7. Typical Capacitance Vs. Drain-Source Voltage

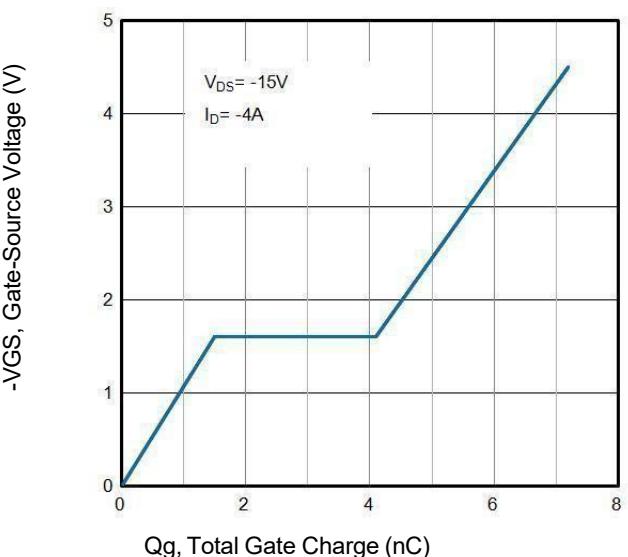


Fig8. Typical Gate Charge Vs. Gate-Source Voltage

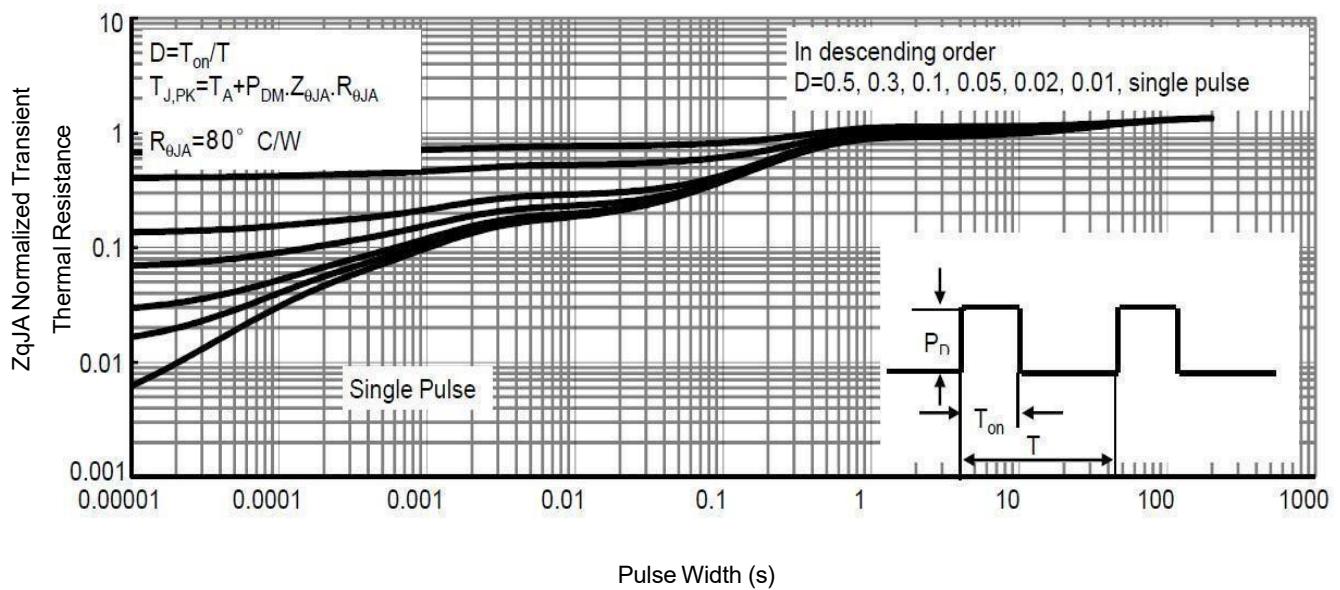


Fig9. Normalized Maximum Transient Thermal Impedance

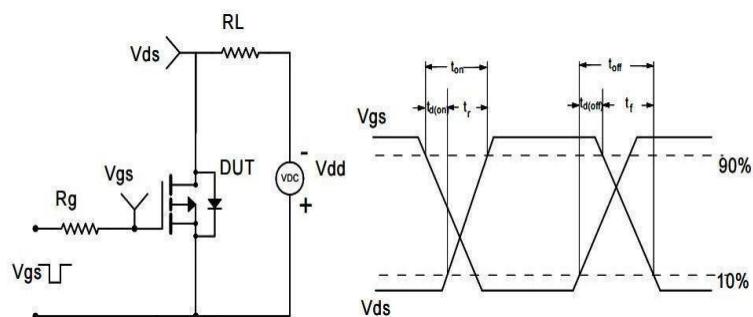
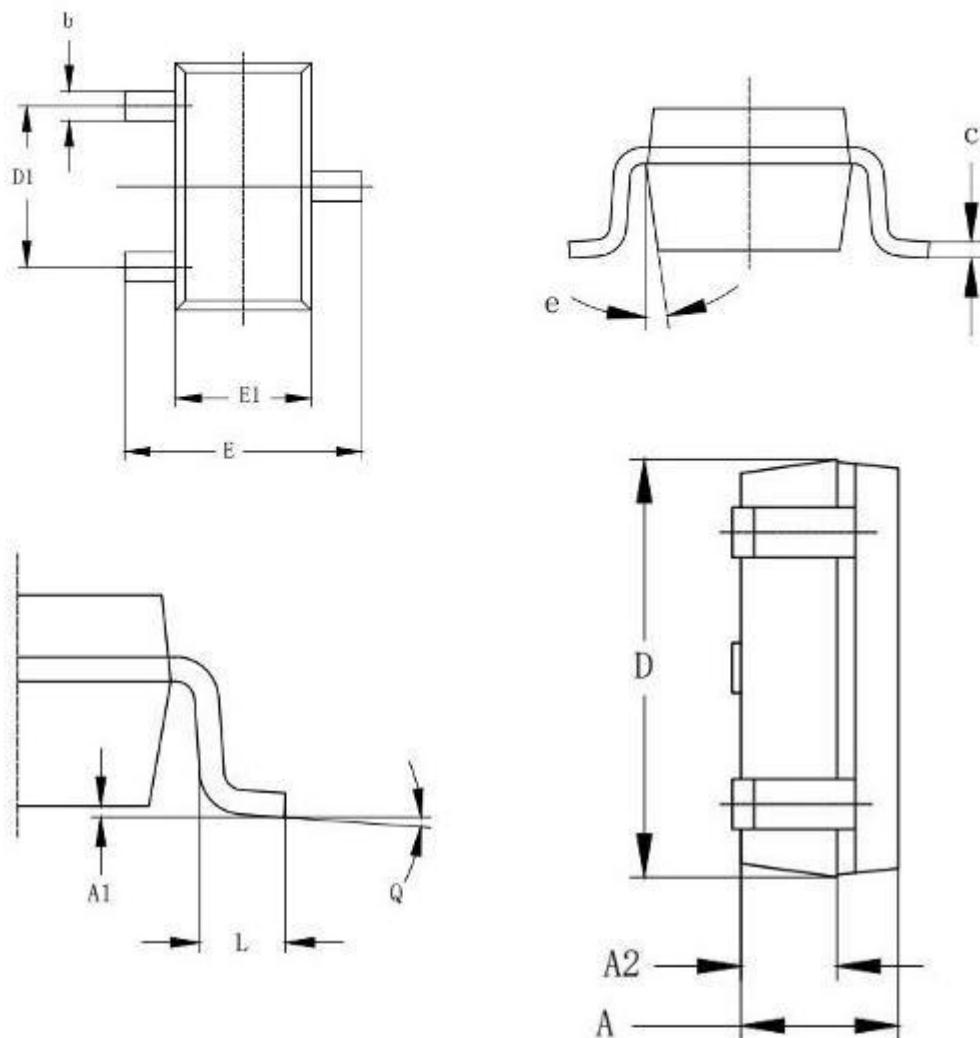


Fig10. Switching Time Test Circuit and waveforms

Package Mechanical Data- SOT23-3



COMMON DIMENSION (MM)			
PKG	SOT23-3L		
SYMBOL	MIN	TYP	MAX
A	1.080	1.100	1.120
A1	0.010	0.060	0.150
A2	0.640	0.670	0.700
b	0.325	0.350	0.375
c	0.125	0.135	0.150
D	2.920	2.930	2.980
D1	1.875	1.900	1.925
E	2.650	2.800	2.950
E1	1.580	1.600	1.670
L	0.300	0.450	0.600
e		8°	
Q	0°	4°	8°