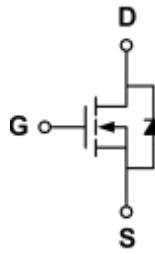
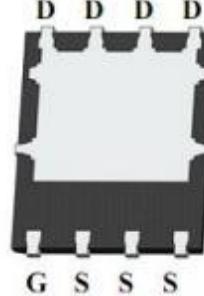


20V N-Channel Enhancement Mode MOSFET

Description	Schematic diagram
<p>The CP2090D6 uses advanced trench technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of $R_{DS(on)}$ and Q_g. This device is ideal for high-frequency switching and synchronous rectification.</p>	
General Features	Marking and pin assignment
<ul style="list-style-type: none"> ◆ $V_{DS} = 20V$, $I_D = 90A$ $R_{DS(on)}(\text{Typ.})=3.2m\Omega$ @ $V_{GS}=4.5V$ $R_{DS(on)}(\text{Typ.})=4.3m\Omega$ @ $V_{GS}=2.5V$ ◆ Excellent gate charge $\times R_{DS(on)}$ product(FOM) ◆ Very low on-resistance $R_{DS(on)}$ ◆ 150 °C operating temperature ◆ 100% UIS tested 	<p>PDFN5*6-8L-A</p> <div style="display: flex; justify-content: space-around;"> <div style="text-align: center;">  <p>Top View</p> <p>XXXXX—Wafer Information YYYYY—Quality Code</p> </div> <div style="text-align: center;">  <p>Bottom View</p> </div> </div> <div style="text-align: center; margin-top: 10px;">    </div>
Application	
<ul style="list-style-type: none"> ◆ Synchronous Rectification in DC/DC and AC/DC Converters ◆ Industrial and Motor Drive applications 	

Ordering Information

Part Number	Storage Temperature	Package	Devices Per Reel
CP2090D6-G	-55°C to +150°C	PDFN5*6-8L-A	5000

Absolute Maximum Ratings (TA=25°C unless otherwise noted)

parameter	symbol	limit	unit
Drain-source voltage	V_{DS}	20	V
Gate-source voltage	V_{GS}	± 12	V
Continuous Drain Current	I_D	90	A
		70	
Pulsed Drain Current	I_{DP}	240	A
Avalanche energy($T_j=25^\circ C$, $V_{DD}=30V$, $V_G=10V$, $L=0.5mH$, $R_g=25\Omega$)	E_{AS}	200	mJ
Power Dissipation	P_D	60	W
		48	
Operating junction Temperature range	T_j	-55—150	°C

Typical Performance Characteristics

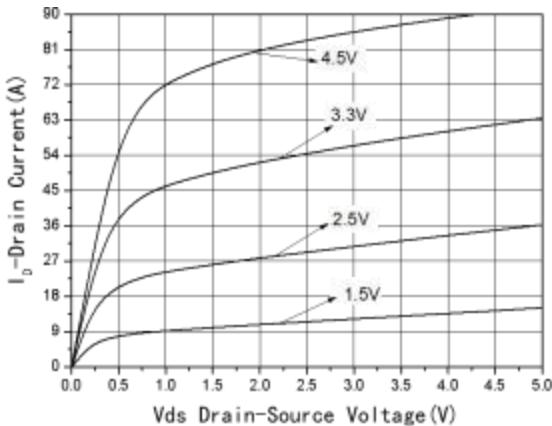


Fig1 Output Characteristics

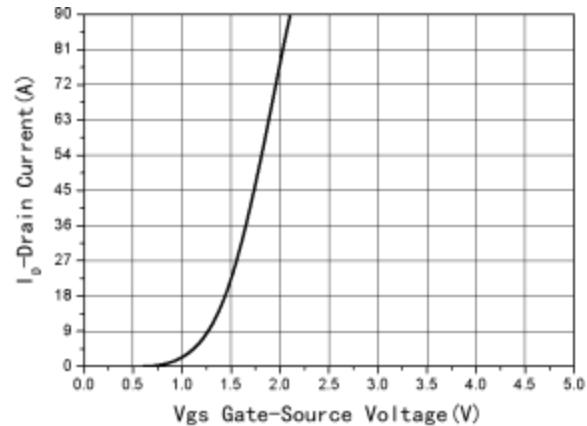


Fig2 Transfer Characteristics

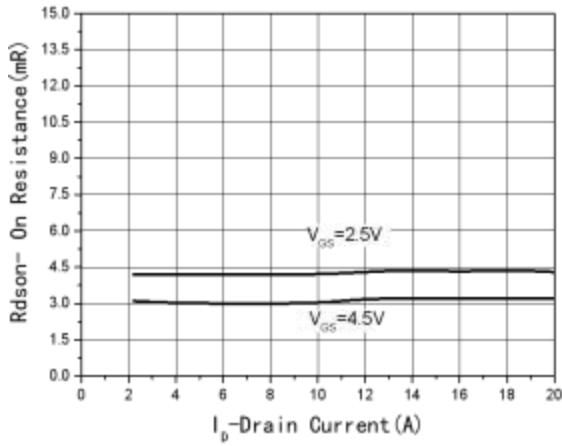


Fig3 Rdson-Drain current

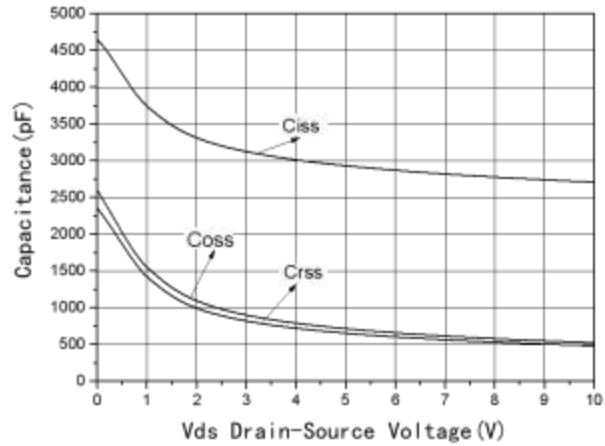


Fig4 Capacitance vs Vds

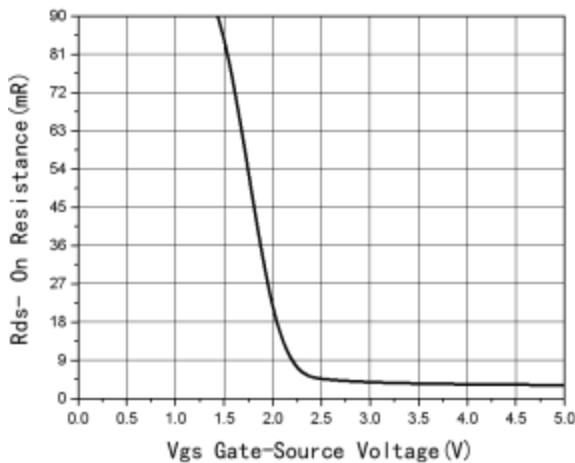


Fig5 Rdson-Gate Drain voltage

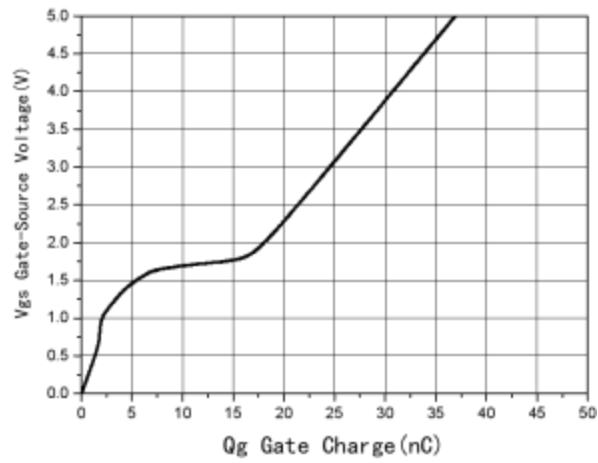


Fig6 Gate Charge

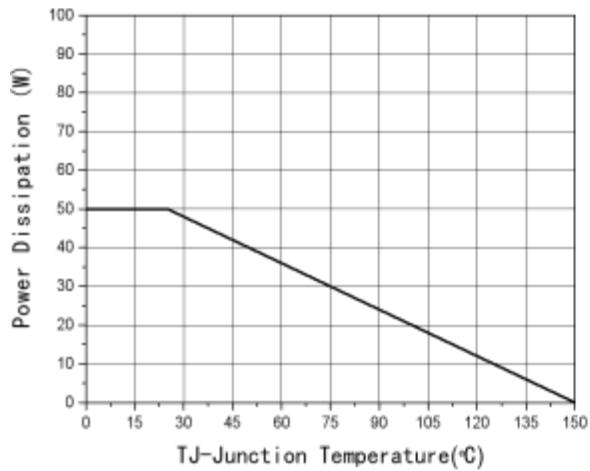


Fig7 Power De-rating

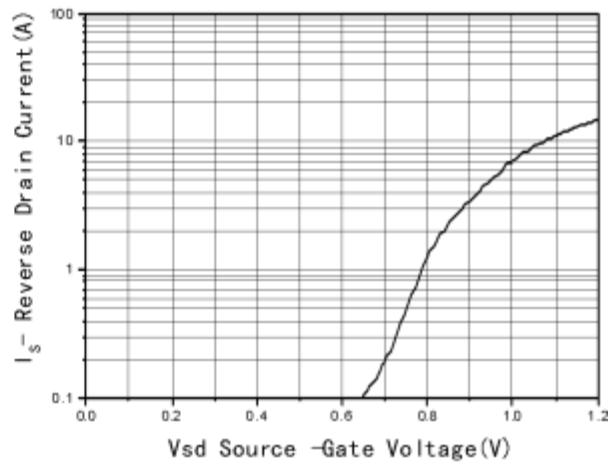
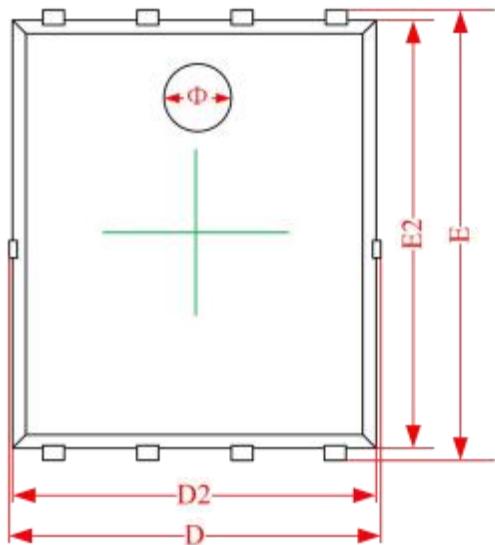
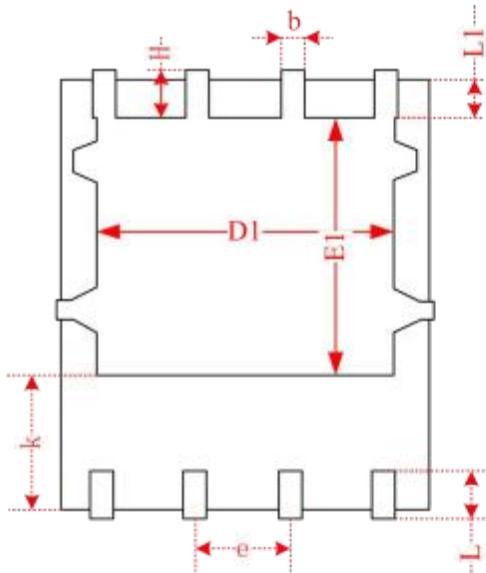
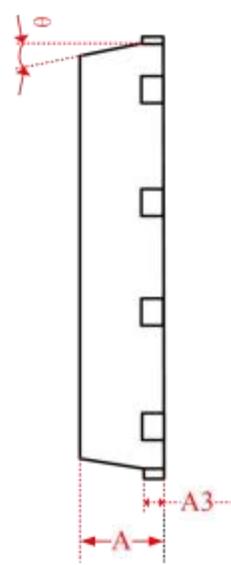


Fig8 Source-Drain Diode Forward

Package Information



PDFN5*6-8L-A

Top ViewBottom ViewSide View

SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.870	0.900	0.930	0.034	0.035	0.036
A3	0.203REF.			0.008REF.		
D	4.944	5.020	5.096	0.195	0.198	0.201
E	5.974	6.050	6.126	0.235	0.238	0.241
D1	3.910	4.010	4.110	0.154	0.158	0.162
E1	3.375	3.475	3.575	0.133	0.137	0.141
D2	4.870	4.900	4.930	0.192	0.193	0.194
E2	5.720	5.750	5.780	0.226	0.227	0.228
k	1.190	1.290	1.390	0.047	0.051	0.055
b	0.350	0.380	0.410	0.014	0.015	0.016
e	1.270TYP.			0.050TYP.		
L	0.559	0.635	0.711	0.022	0.025	0.028
L1	0.424	0.500	0.576	0.017	0.020	0.023
H	0.574	0.650	0.726	0.023	0.026	0.029
θ	10°	11°	12°	10°	11°	12°
Φ	1.150	1.200	1.250	0.045	0.047	0.049