

### Description

The CP2301CI uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

### General Features

$V_{DS} = -20V$   $I_D = -2.3A$

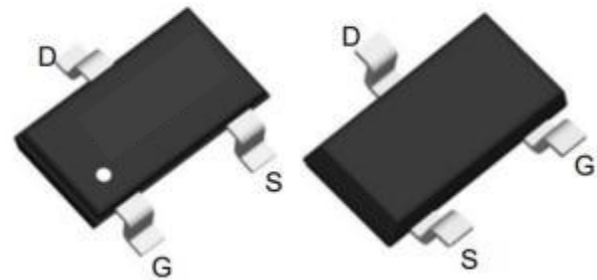
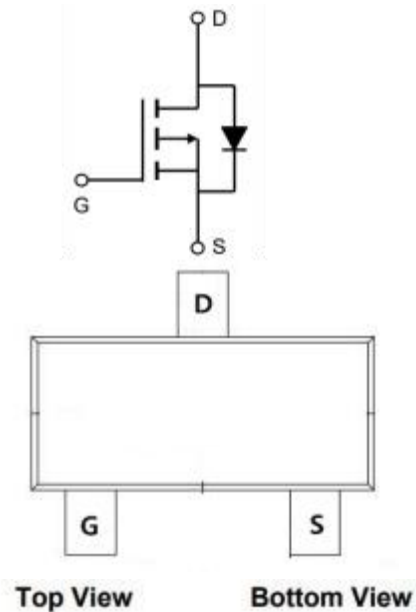
$R_{DS(ON)} < 150m\Omega$  @  $V_{GS} = -4.5V$  (Type: 125m $\Omega$ )

### Application

Battery protection

Load switch

Uninterruptible power supply



### Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
CP2301CI	SOT23L	A1SHB..	3000

### Absolute Maximum Ratings ( $T_c = 25^\circ C$ unless otherwise noted)

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	-20	V
$V_{GS}$	Gate-Source Voltage	$\pm 12$	V
$I_D @ T_A = 25^\circ C$	Continuous Drain Current, $V_{GS} @ -4.5V^1$	-2.3	A
$I_D @ T_A = 70^\circ C$	Continuous Drain Current, $V_{GS} @ -4.5V^1$	-0.9	A
$I_{DM}$	Pulsed Drain Current <sup>2</sup>	-7.4	A
$P_D @ T_A = 25^\circ C$	Total Power Dissipation <sup>3</sup>	1.1	W
$P_D @ T_A = 70^\circ C$	Total Power Dissipation <sup>3</sup>	0.6	W
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ C$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ C$
$R_{\theta JA}$	Thermal Resistance Junction-Ambient <sup>1</sup>	125	$^\circ C/W$
$R_{\theta JC}$	Thermal resistance, junction-case	32	$^\circ C/W$

### Electrical Characteristics (T<sub>J</sub>=25°C, unless otherwise noted)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
V(BR)DSS	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> = -250μA	-20	-	-	V
IDSS	Zero Gate Voltage Drain Current	V <sub>DS</sub> = -20V, V <sub>GS</sub> = 0V,	-	-	-1	μA
IGSS	Gate to Body Leakage Current	V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±12V	-	-	±100	nA
VGS(th)	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250μA	-0.4	-0.7	-1.0	V
RDS(on)	Static Drain-Source on-Resistance	V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -2A	-	125	150	mΩ
		V <sub>GS</sub> = -2.5V, I <sub>D</sub> = -1A	-	145	180	
Ciss	Input Capacitance	V <sub>DS</sub> = -10V, V <sub>GS</sub> = 0V, f = 1.0MHz	-	145	-	pF
Coss	Output Capacitance		-	33	-	pF
Crss	Reverse Transfer Capacitance		-	23	-	pF
Qg	Total Gate Charge	V <sub>DS</sub> = -10V, I <sub>D</sub> = -2A, V <sub>GS</sub> = -4.5V	-	4.5	-	nC
Qgs	Gate-Source Charge		-	0.85	-	nC
Qgd	Gate-Drain("Miller") Charge		-	1.4	-	nC
td(on)	Turn-on Delay Time	V <sub>DD</sub> = -10V, R <sub>L</sub> =5Ω, R <sub>GEN</sub> =3Ω, V <sub>GS</sub> =-4.5V,	-	6	-	ns
tr	Turn-on Rise Time		-	30	-	ns
td(off)	Turn-off Delay Time		-	45	-	ns
tr	Turn-off Fall Time		-	46	-	ns
IS	Maximum Continuous Drain to Source Diode Forward Current		-	-	-2.3	A
ISM	Maximum Pulsed Drain to Source Diode Forward Current		-	-	-8	A
VSD	Drain to Source Diode Forward Voltage	V <sub>GS</sub> = 0V, I <sub>S</sub> = -2A	-	-	-1.2	V

Note :

- 1、The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 20Z copper.
- 2、The data tested by pulsed , pulse width Δ 300us , duty cycle Δ 2%
- 3、The power dissipation is limited by 150°C junction temperature
- 4、The data is theoretically the same as I<sub>D</sub> and I<sub>DM</sub> , in real applications , should be limited by total power dissipation.

### Typical Characteristics

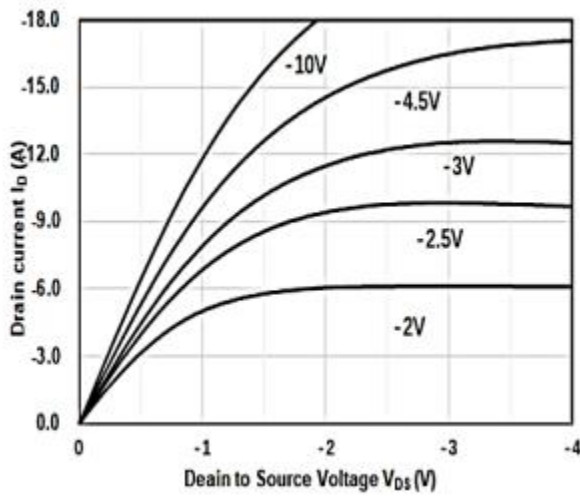


Figure1: Output Characteristics

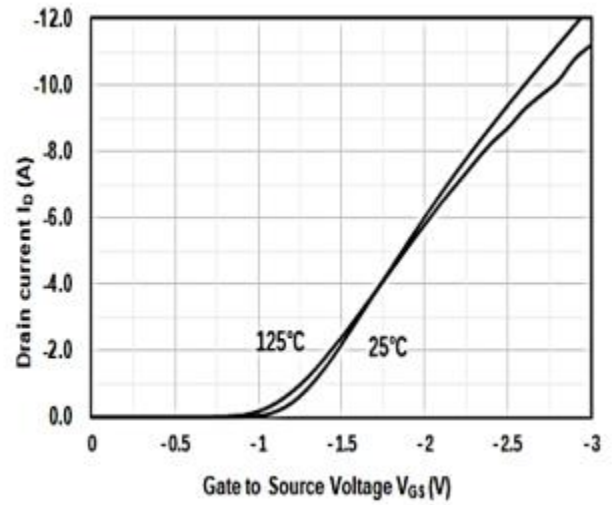


Figure 2: Typical Transfer Characteristics

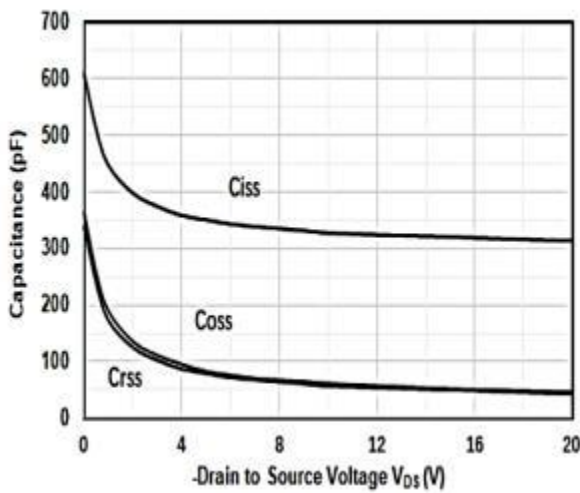


Figure 3: Capacitance Characteristics

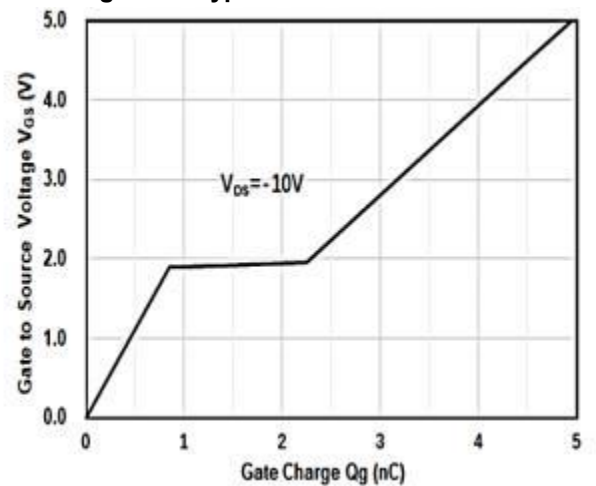


Figure 4: Gate Charge

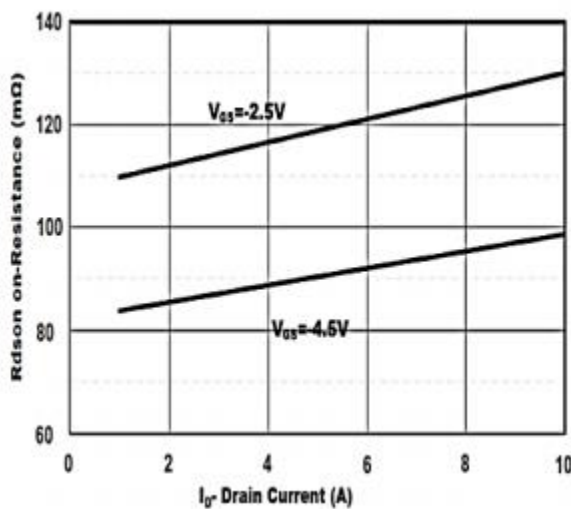


Figure 5: Drain-Source on Resistance

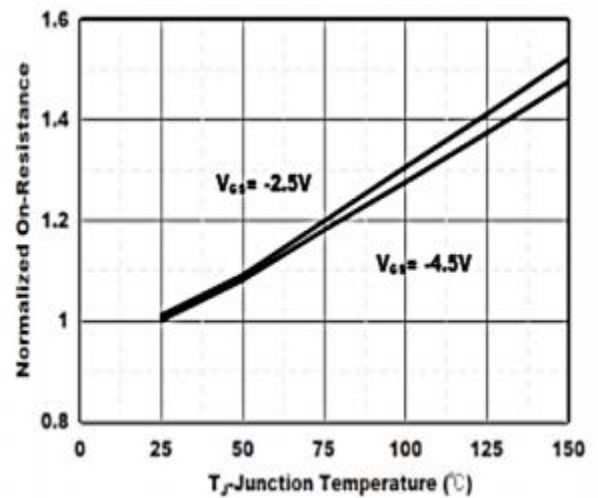


Figure 6: Drain-Source on Resistance

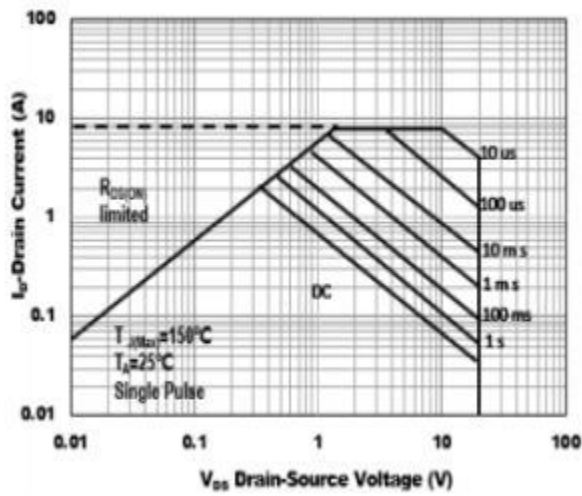


Figure 7: Safe Operation Area

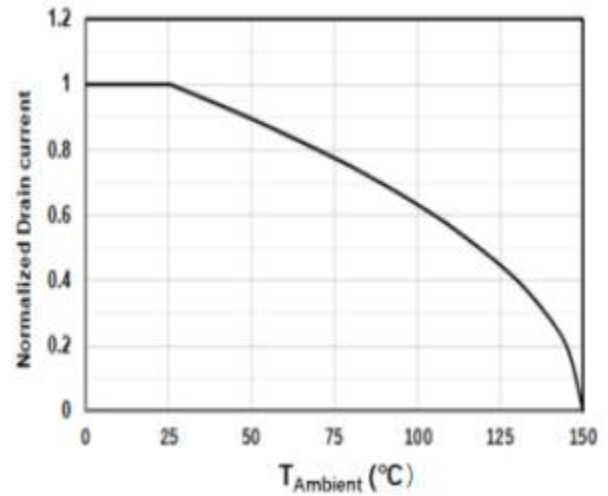


Figure 8: Drain Current vs Ambient temperature

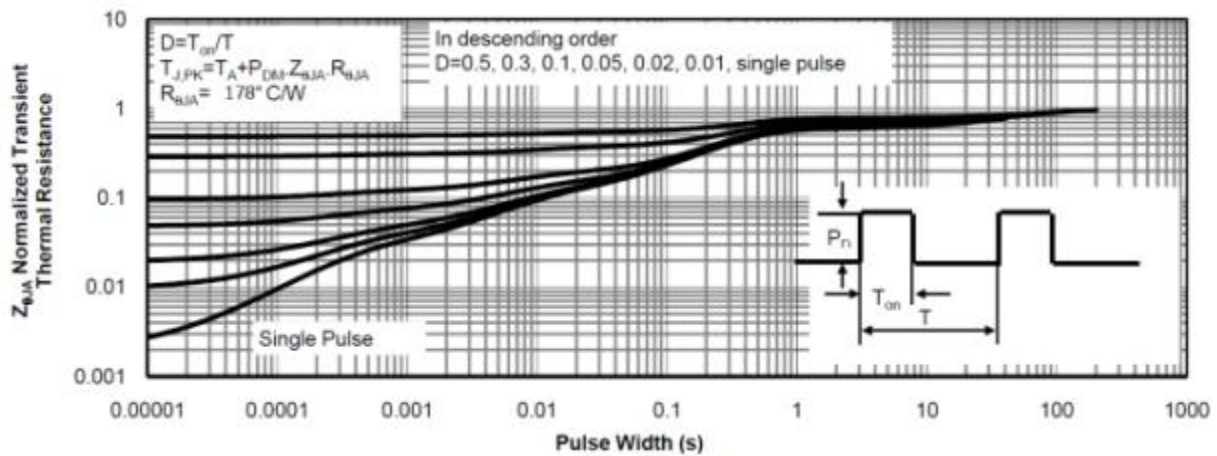
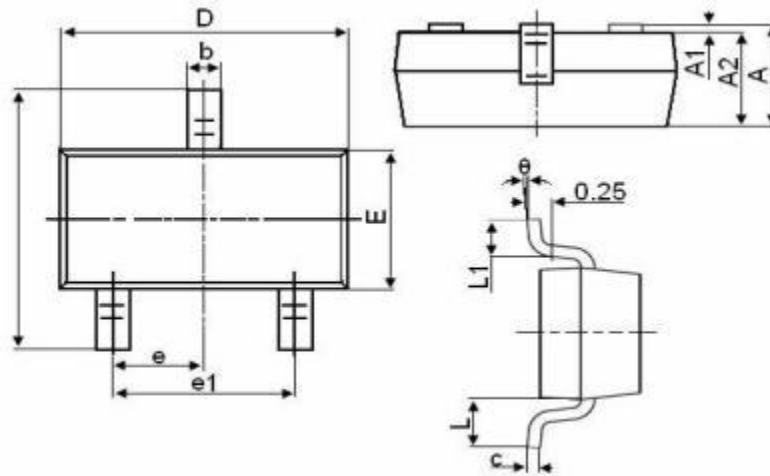


Figure 9: Normalized Maximum Transient Thermal Impedance

### Package Mechanical Data-SOT23-XC-Single



Symbol	Dimensions in Millimeters	
	MIN.	MAX.
A	0.900	1.150
A1	0.000	0.100
A2	0.900	1.050
b	0.300	0.500
c	0.080	0.150
D	2.800	3.000
E	1.200	1.400
E1	2.250	2.550
e	0.950TYP	
e1	1.800	2.000
L	0.550REF	
L1	0.300	0.500
θ	0°	8°

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# CP2301CI

## -20V P-Channel Enhancement Mode MOSFET

Edition	Date	Change
REV1.0	2021/4/31	Initial release

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