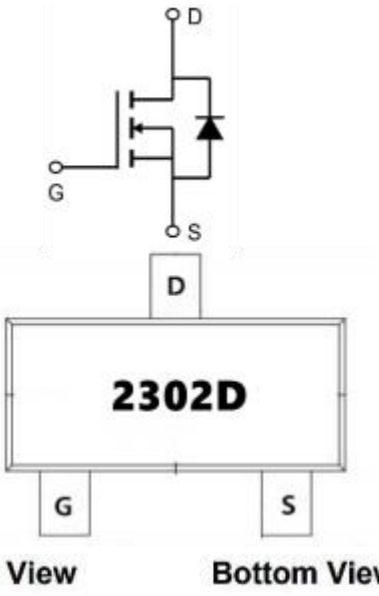
CP2302DI

20V N-Channel Enhancement Mode MOSFET

Description

The CP2302DI uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a Battery protection or in other Switching application.



Top View Bottom View

General Features

V_{DS} = 20V I_D =2.3A

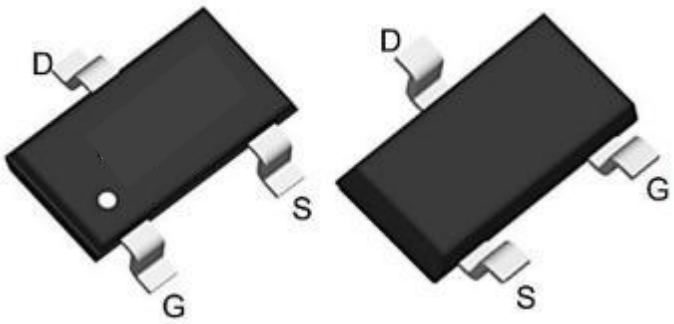
$R_{DS(ON)}$ < 65mΩ @ V_{GS}=10V **（Type：52mΩ）**

Application

Battery protection

Load switch

Uninterruptible power supply



Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
CP2302DI	SOT23L	2302D	3000

Absolute Maximum Ratings (T_C=25℃unless otherwise noted)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	20	V
V_{GS}	Gate-Source Voltage	±12	V
I_D@T_A=25℃	Continuous Drain Current, V_{GS} @ 4.5V[1]	2.3	A
I_D@T_A=70℃	Continuous Drain Current, V_{GS} @ 4.5V[1]	1.2	A
I_{DM}	Pulsed Drain Current[2]	6.9	A
P_D@T_A=25℃	Total Power Dissipation[3]	0.77	W
T_{STG}	Storage Temperature Range	-55 to 150	℃
T_J	Operating Junction Temperature Range	-55 to 150	℃
$R_{\theta JA}$	Thermal Resistance Junction-ambient [1]	125	℃/W
$R_{\theta JC}$	Thermal Resistance Junction-Case[1]	100	℃/W

Electrical Characteristics (T_J=25℃, unless otherwise noted)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
V(BR)DSS	Drain-Source Breakdown Voltage	V_{GS}=0V,I_D=250μA	20	22	-	V
IDSS	Zero Gate Voltage Drain Current	V_{DS} =20V, V_{GS} = 0V,	-	-	1.0	μA
IGSS	Gate to Body Leakage Current	V_{DS} =0V,V_{GS} = ±12V	-	-	±100	nA
VGS(th)	Gate Threshold Voltage	V_{DS}= V_{GS}, I_D=250μA	0.5	0.65	1.2	V
RDS(on)	Static Drain-Source on-Resistance note2	V_{GS} =4.5V, I_D =3A	-	52	65	mΩ
		V_{GS} =2.5V, I_D =2A	-	75	90	
C_{iss}	Input Capacitance	V_{DS} =10V, V_{GS} =0V, f = 1.0MHz	-	150	-	pF
Coss	Output Capacitance		-	34	-	pF
Crss	Reverse Transfer Capacitance		-	26	-	pF
Q_g	Total Gate Charge	V_{DS} =10V, I_D =3A, V_{GS} =4.5V	-	2.4	-	nC
Q_{gs}	Gate-Source Charge		-	0.88	-	nC
Q_{gd}	Gate-Drain(“Miller”) Charge		-	0.77	-	nC
td(on)	Turn-on Delay Time	V_{DS}=10V, I_D=3A, R_{GEN}=3Ω, V_{GS} =4.5V	-	6.8	-	ns
t_r	Turn-on Rise Time		-	57	-	ns
td(off)	Turn-off Delay Time		-	14	-	ns
t_f	Turn-off Fall Time		-	53	-	ns
IS	Maximum Continuous Drain to Source Diode ForwardCurrent		-	-	2.3	A
ISM	Maximum Pulsed Drain to Source Diode Forward Current		-	-	6.8	A
VSD	Drain to Source Diode Forward Voltage	V_{GS} = 0V, I_S=3A	-	-	1.3	V

Note :

1、The data tested by surface mounted on a 1 inch^2 FR-4 board with 2OZ copper.

2、The data tested by pulsed , pulse width ≦ 300us , duty cycle ≦ 2%

3、The power dissipation is limited by 150℃ junction temperature

4、The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

Typical Characteristics

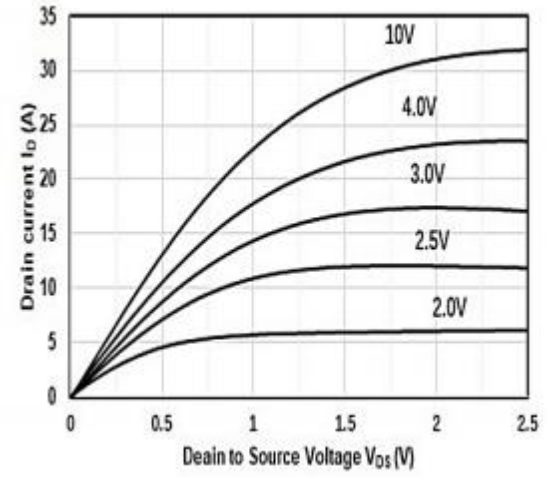


Figure1. Output Characteristics

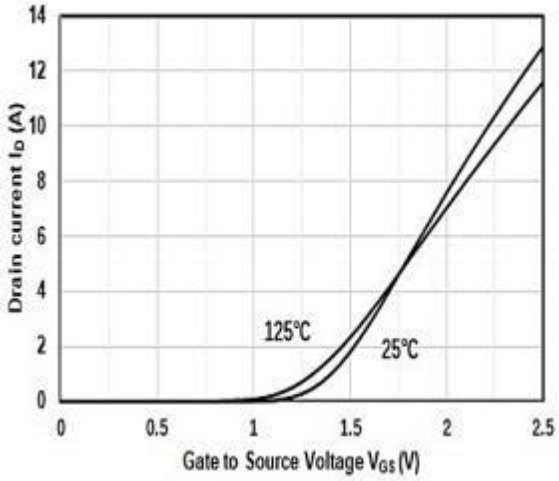


Figure2. Transfer Characteristics

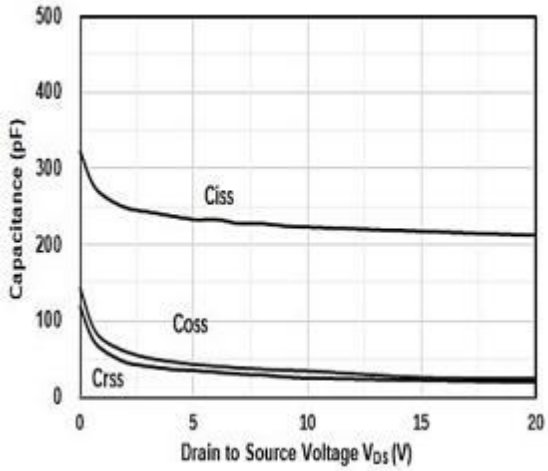


Figure3. Capacitance Characteristics

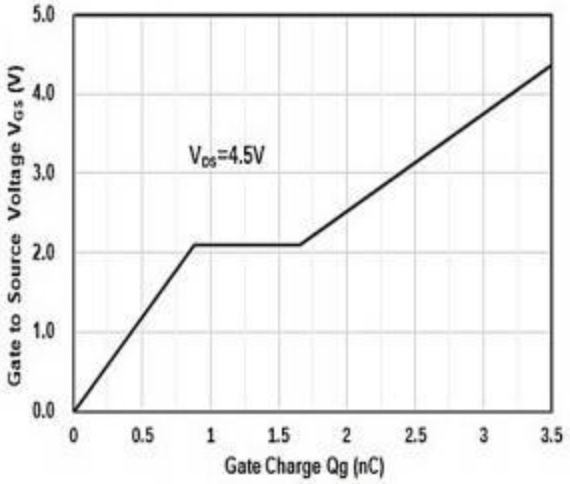


Figure4. Gate Charge

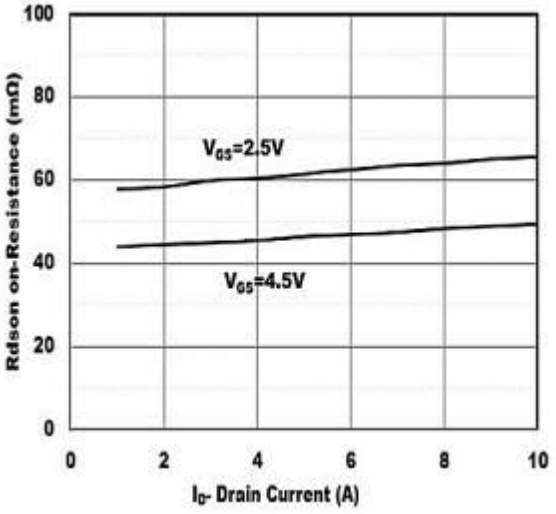


Figure5. Drain-Source on Resistance

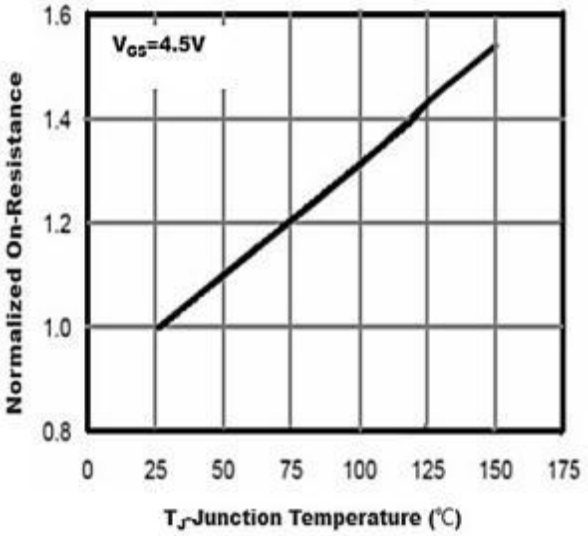
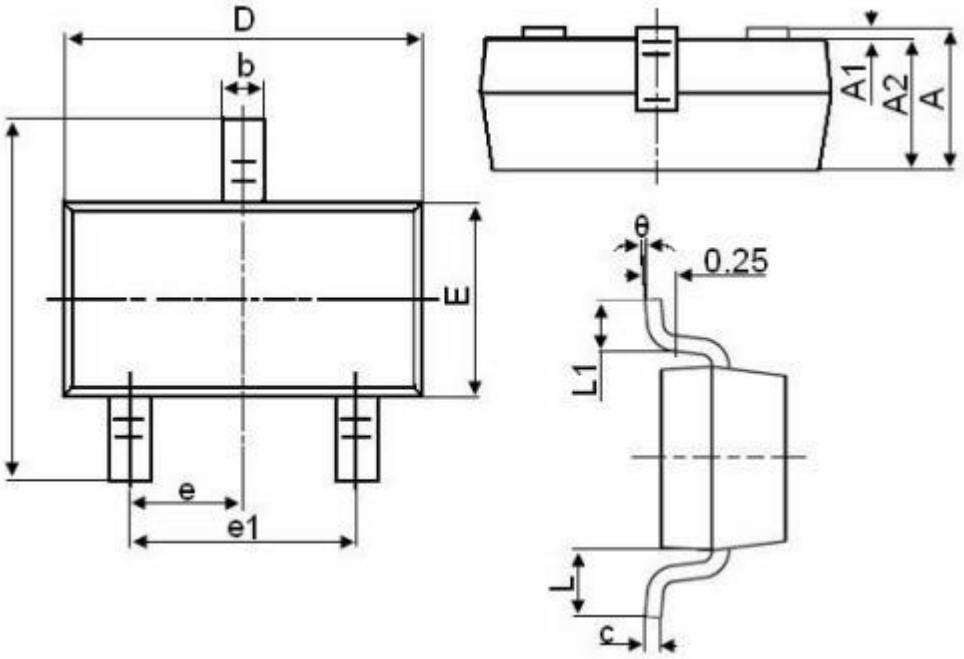


Figure6. Drain-Source on Resistance

Package Mechanical Data-SOT23-XC-Single



Symbol	Dimensions in Millimeters	
	MIN.	MAX.
A	0.900	1.150
A1	0.000	0.100
A2	0.900	1.050
b	0.300	0.500
c	0.080	0.150
D	2.800	3.000
E	1.200	1.400
E1	2.250	2.550
e	0.950TYP	
e1	1.800	2.000
L	0.550REF	
L1	0.300	0.500
θ	0°	8°

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Edition	Date	Change
Rve1.0	2022/1/1	Initial release