

30V N-Channel Enhancement Mode MOSFET

Description

The CP4406QR uses trench MOSFET technology that is uniquely optimized to provide the most efficient high frequency switching performance. Conduction and switching losses are minimized due to an extremely low combination of $R_{DS(ON)}$ and C_{rss} .

General Features

- ◆ $V_{DS} = 30V$, $I_D = 25A$
 $R_{DS(ON)}(Typ.) = 8.7m\Omega$ @ $V_{GS} = 10V$
 $R_{DS(ON)}(Typ.) = 13.3m\Omega$ @ $V_{GS} = 4.5V$
- ◆ Lead free product is acquired
- ◆ Surface mount package

Application

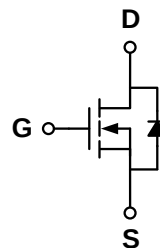
- ◆ High Frequency Point-of-Load Synchronous Buck Converter for MB/NB/UMPC/VGA
- ◆ Networking DC-DC Power System
- ◆ Load switch

Package

- ◆ PDFN3X3-8L



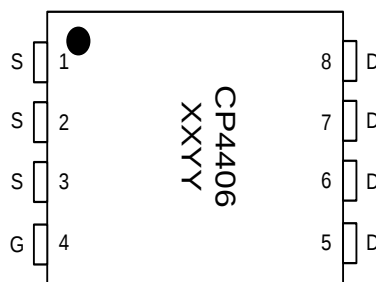
Schematic diagram



Marking and pin assignment

PDFN3×3-8L

(Top View)



Ordering Information

Part Number	Storage Temperature	Package	Devices Per Reel
CP4406QR-G	-55°C to +150°C	PDFN3×3-8L	5000

Absolute Maximum Ratings (TA=25°C unless otherwise noted)

parameter		symbol	limit	unit
Drain-source voltage		V_{DS}	30	V
Gate-source voltage		V_{GS}	±20	V
Continuous Drain Current	TC=25°C	I_D	25	A
	TC=70°C		20	
Pulsed Drain Current		I_{DP}	100	A
Avalanche energy(L=0.1mH)		E_{AS}	80	mJ
Power Dissipation	TC=25°C	P_D	40	W
	TC=70°C		26	
Operating junction Temperature range		T_j	-55—150	°C

Electrical Characteristics (TA=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Static Characteristics						
Drain-source breakdown voltage	BV _{DSS}	V _{GS} =0V, I _D =250μA	30	-	-	V
Zero gate voltage drain current	I _{DSS}	V _{DS} =30V, V _{GS} =0V	-	-	1	μA
		T _J =85°C	-	-	5	
Gate Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V	-	-	±100	nA
Gate threshold voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	1	1.6	2.5	V
Drain-source on-state resistance ¹	R _{DS(ON)}	V _{GS} =10V, I _D =10A	-	8.7	10	mΩ
		V _{GS} =4.5V, I _D =10A	-	13.3	17	
On Status Drain Current	I _{D(ON)}	V _{DS} =10V, V _{GS} =5V	50	-	-	A
Diode Characteristics						
Diode Forward Voltage	V _{SD}	I _{SD} =1A, V _{GS} =0V	-	0.82	1.1	V
Diode Continuous Forward Current	I _S		-	-	4	A
Reverse Recovery Time	t _{rr}	I _F =10A,	-	28	-	ns
Reverse Recovery Charge	Q _{rr}	dI/dt=100A/μs	-	40	-	nC
Dynamic Characteristics						
Gate Resistance	R _G	V _{GS} =0V, V _{DS} =0V, f=1MHz	-	1.9	-	Ω
Input capacitance	C _{ISS}	V _{GS} =0V, V _{DS} =15V f=1.0MHz	-	992	-	pF
Output capacitance	C _{OSS}		-	131	-	
Reverse transfer capacitance	C _{RSS}		-	113	-	
Turn-on delay time	t _{D(ON)}	V _{GS} =10V, V _{DS} =15V, R _L =1Ω, R _G =3Ω	-	4.4	-	ns
Turn-on Rise time	t _r		-	9	-	
Turn-off delay time	t _{D(OFF)}		-	17	-	
Turn-off Fall time	t _f		-	6	-	
Total gate charge	Q _g	V _{GS} =10V, V _{DS} =15V, I _D =10A	-	19.5	-	nC
Gate-source charge	Q _{gs}		-	3.3	-	
Gate-drain charge	Q _{gd}		-	3.6	-	

Thermal Characteristics

Parameter	Symbol	Typ	Max	Unit
Maximum Junction-to-Ambient ^A	R _{θJA}	33	40	°C/W
Maximum Junction-to-Ambient ^A		59	75	
Maximum Junction-to-Lead ^B	R _{θJC}	5	6	

A: The value of R_{θJA} is measured with the device mounted on 1 in² FR-4 board with 2oz. Copper, in a still air environment with T_A=25°C. The value in any given application depends on the user's specific board design. The current rating is based on the t ≤ 10s thermal resistance rating.

B: The R_{θJA} is the sum of the thermal impedance from junction to lead R_{θJC} and lead to ambient.

Typical Performance Characteristics

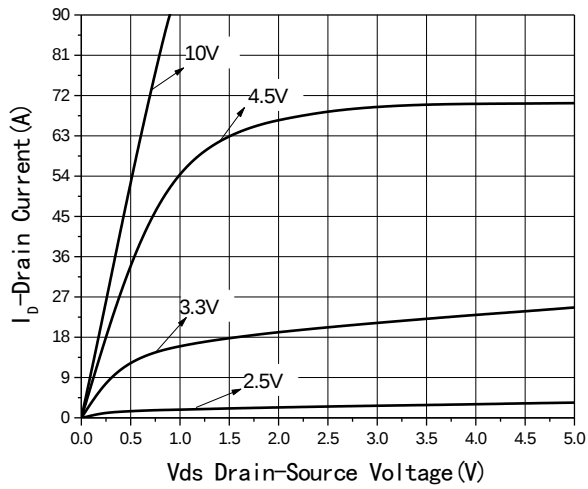


Fig1 Output Characteristics

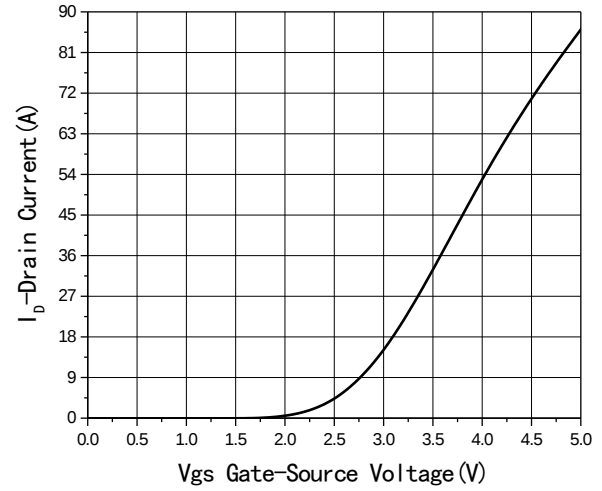


Fig2 Transfer Characteristics

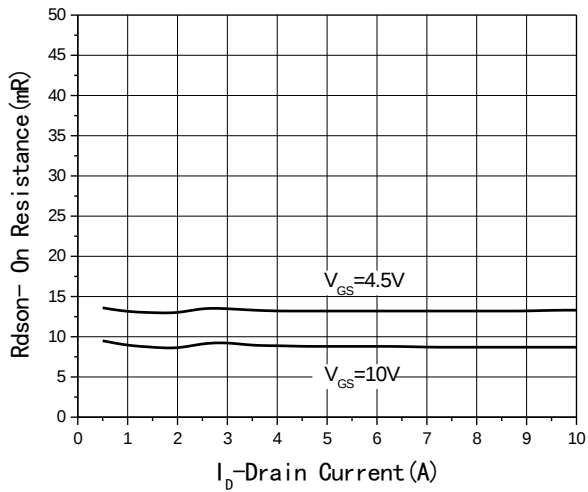


Fig3 Rdson-Drain current

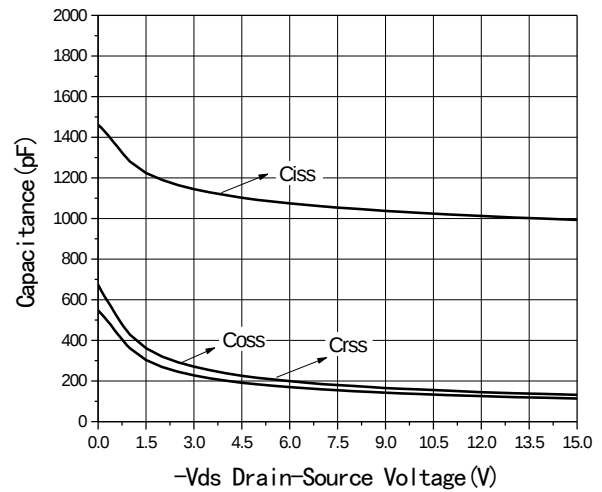


Fig4 Capacitance vs Vds

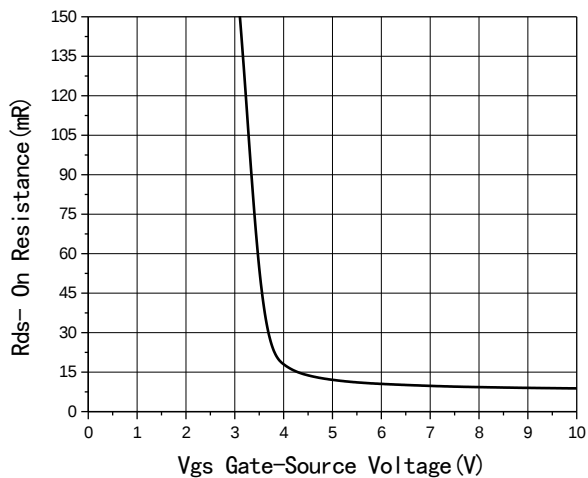


Fig5 Rdson-Gate Drain voltage

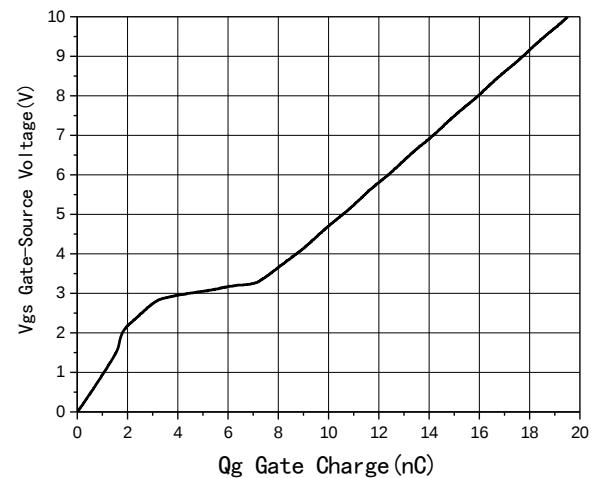
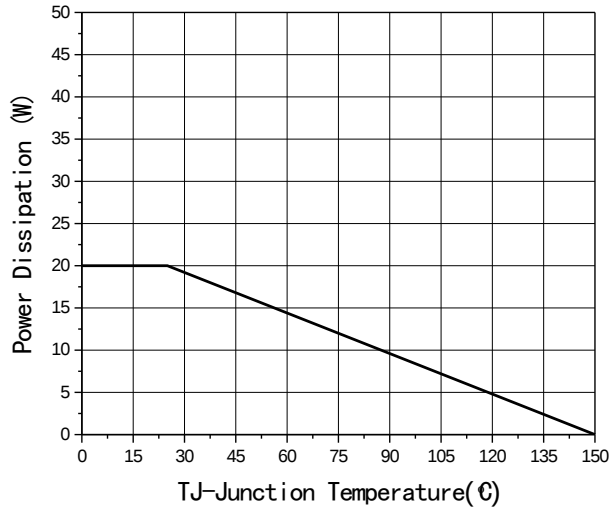
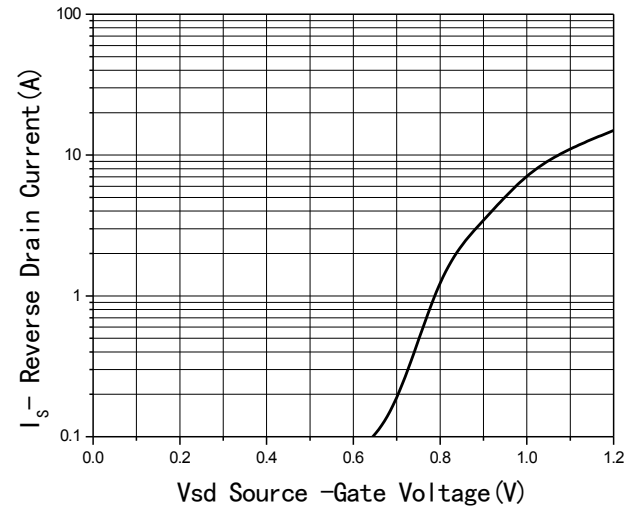


Fig6 Gate Charge


Fig7 Power De-rating

Fig8 Source-Drain Diode Forward

Package Information

- PDFN3×3-8L

