

Dual N-Ch 30V Fast Switching MOSFETs

- ☒ Super Low Gate Charge
- ☒ 100% EAS Guaranteed
- ☒ Green Device Available
- ☒ Excellent CdV/dt effect decline
- ☒ Advanced high cell density Trench technology

Product Summary



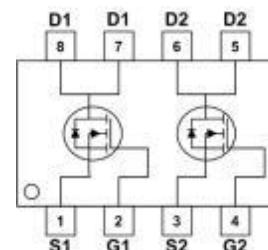
| BVDSS | RDS(on) | ID |
|-------|---------|------|
| 30V | 20mΩ | 9.5A |

Description

The CP4822 is the high performance complementary N-chand P-ch MOSFETs with high cell density, which provide excellent RDS(on) and gate charge for most of the synchronous buck converter applications.

The CP4822 meet the RoHS and Green Product requirement 100% EAS guaranteed with full function reliability approved.

SOP8 Pin Configuration



Absolute Maximum Ratings

| Symbol | Parameter | Rating | Units |
|--------------------------------------|--|------------|-------|
| V _{DS} | Drain-Source Voltage | 30 | V |
| V _{GS} | Gate-Source Voltage | ±20 | V |
| I _D @T _A =25°C | Continuous Drain Current, V _{GS} @ 10V ¹ | 9.5 | A |
| I _D @T _A =70°C | Continuous Drain Current, V _{GS} @ 10V ¹ | 5.8 | A |
| I _{DM} | Pulsed Drain Current ² | 37 | A |
| EAS | Single Pulse Avalanche Energy ³ | 12.1 | mJ |
| I _{AS} | Avalanche Current | -- | A |
| P _D @T _A =25°C | Total Power Dissipation ⁴ | 2.8 | W |
| T _{STG} | Storage Temperature Range | -55 to 150 | °C |
| T _J | Operating Junction Temperature Range | -55 to 150 | °C |

Thermal Data

| Symbol | Parameter | Typ. | Max. | Unit |
|------------------|--|------|------|------|
| R _{θJA} | Thermal Resistance Junction-ambient ¹ | --- | --- | °C/W |
| R _{θJC} | Thermal Resistance Junction-Case ¹ | --- | 46 | °C/W |

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Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise specified)

| Symbol | Parameter | Test Condition | Min. | Typ. | Max. | Units |
|---|--|---|------|------|-----------|------------------|
| Off Characteristics | | | | | | |
| $V_{(\text{BR})\text{DSS}}$ | Drain-Source Breakdown Voltage | $V_{\text{GS}}=0\text{V}$, $I_D=250\mu\text{A}$ | 30 | - | - | V |
| I_{DSS} | Zero Gate Voltage Drain Current | $V_{\text{DS}}=30\text{V}$, $V_{\text{GS}}=0\text{V}$, | - | - | 1.0 | μA |
| I_{GSS} | Gate to Body Leakage Current | $V_{\text{DS}}=0\text{V}$, $V_{\text{GS}}=\pm 20\text{V}$ | - | - | ± 100 | nA |
| On Characteristics | | | | | | |
| $V_{\text{GS}(\text{th})}$ | Gate Threshold Voltage | $V_{\text{DS}}=V_{\text{GS}}$, $I_D=250\mu\text{A}$ | 1.0 | 1.5 | 2.5 | V |
| $R_{\text{DS}(\text{on})}$ note3 | Static Drain-Source on-Resistance | $V_{\text{GS}}=10\text{V}$, $I_D=5\text{A}$ | - | 20 | 26 | $\text{m}\Omega$ |
| | | $V_{\text{GS}}=4.5\text{V}$, $I_D=3\text{A}$ | - | 24 | 31 | |
| Dynamic Characteristics | | | | | | |
| C_{iss} | Input Capacitance | $V_{\text{DS}}=15\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1.0\text{MHz}$ | - | 490 | - | pF |
| C_{oss} | Output Capacitance | | - | 79 | - | pF |
| C_{rss} | Reverse Transfer Capacitance | | - | 61 | - | pF |
| Q_g | Total Gate Charge | $V_{\text{DS}}=15\text{V}$, $I_D=5.8\text{A}$, $V_{\text{GS}}=10\text{V}$ | - | 10 | - | nC |
| Q_{gs} | Gate-Source Charge | | - | 1.7 | - | nC |
| Q_{gd} | Gate-Drain("Miller") Charge | | - | 2.5 | - | nC |
| Switching Characteristics | | | | | | |
| $t_{\text{d}(\text{on})}$ | Turn-on Delay Time | $V_{\text{DS}}=15\text{V}$, $I_D=3\text{A}$, $V_{\text{GS}}=10\text{V}$, $R_{\text{REN}}=3\Omega$ | - | 6 | - | ns |
| t_r | Turn-on Rise Time | | - | 15 | - | ns |
| $t_{\text{d}(\text{off})}$ | Turn-off Delay Time | | - | 17 | - | ns |
| t_f | Turn-off Fall Time | | - | 17 | - | ns |
| Drain-Source Diode Characteristics and Maximum Ratings | | | | | | |
| I_s | Maximum Continuous Drain to Source Diode Forward Current | | - | - | 9 | A |
| I_{SM} | Maximum Pulsed Drain to Source Diode Forward Current | | - | - | 36 | A |
| V_{SD} | Drain to Source Diode Forward Voltage | $V_{\text{GS}}=0\text{V}$, $I_s=9\text{A}$ | - | - | 1.2 | V |
| trr | Body Diode Reverse Recovery Time | $I_f=5\text{A}$, $dI/dt=100\text{A}/\mu\text{s}$ | - | 7 | - | ns |
| Q_{rr} | Body Diode Reverse Recovery Charge | | - | 2 | - | nC |

Notes: 1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. EAS condition : $T_J=25^\circ\text{C}$, $V_{\text{DD}}=15\text{V}$, $V_{\text{G}}=10\text{V}$, $L=0.5\text{mH}$, $R_g=25\Omega$, $I_{\text{AS}}=6\text{A}$

3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 0.5\%$

Typical Performance Characteristics

Figure 1: Output Characteristics

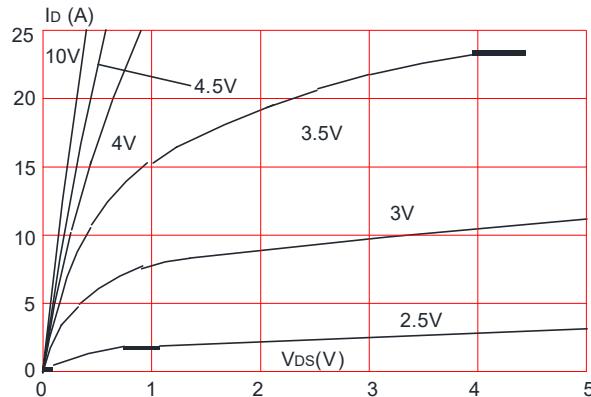


Figure 3: On-resistance vs. Drain Current

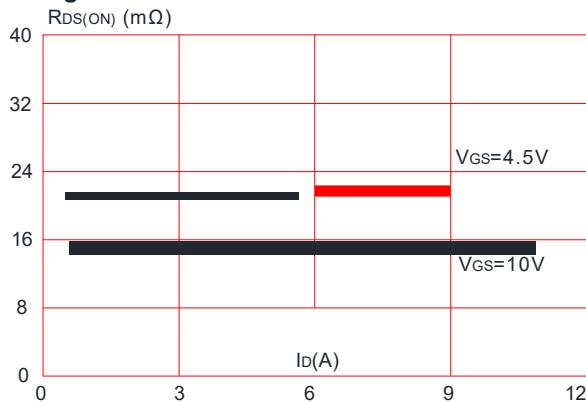


Figure 5: Gate Charge Characteristics

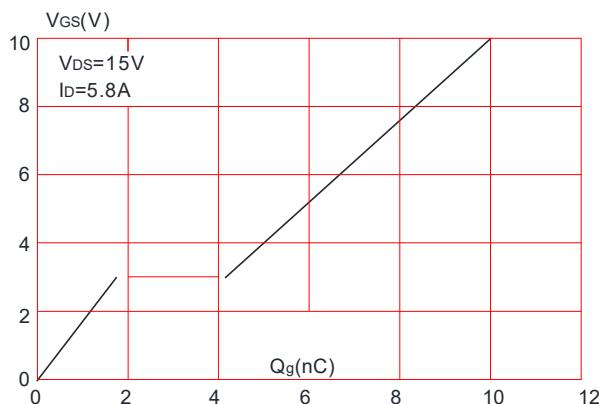


Figure 2: Typical Transfer Characteristics

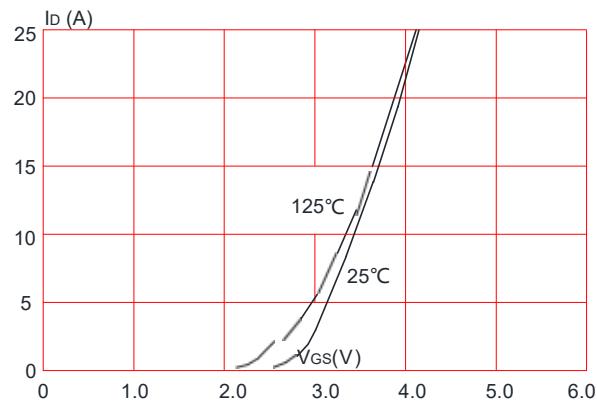


Figure 4: Body Diode Characteristics

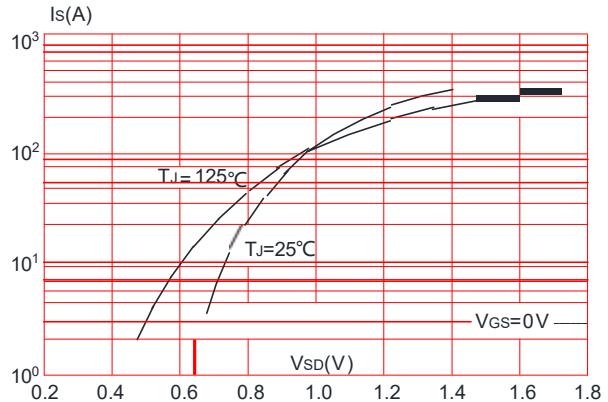
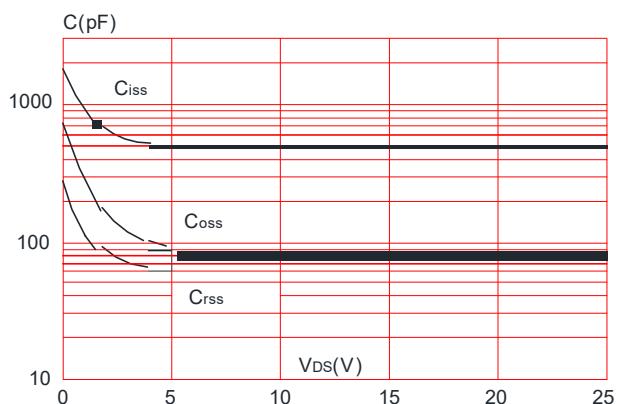


Figure 6: Capacitance Characteristics



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Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

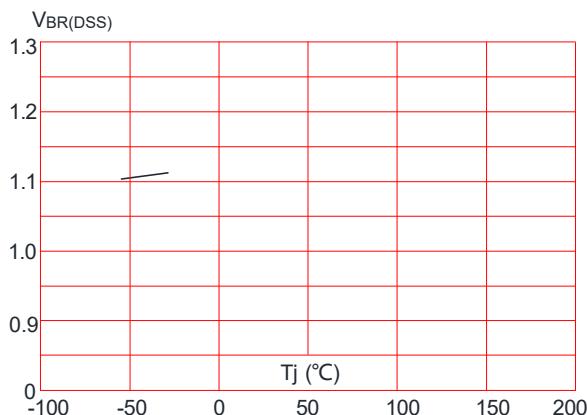


Figure 9: Maximum Safe Operating Area

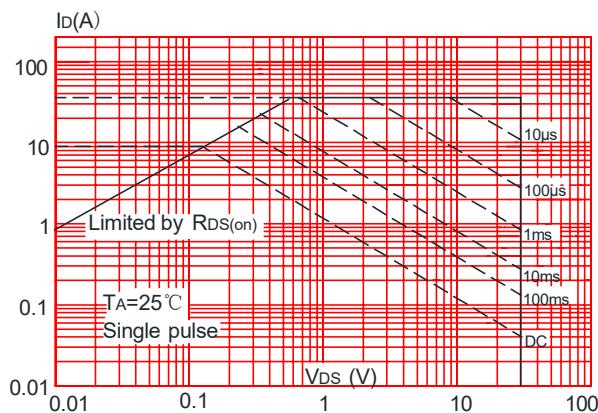


Figure 11: Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

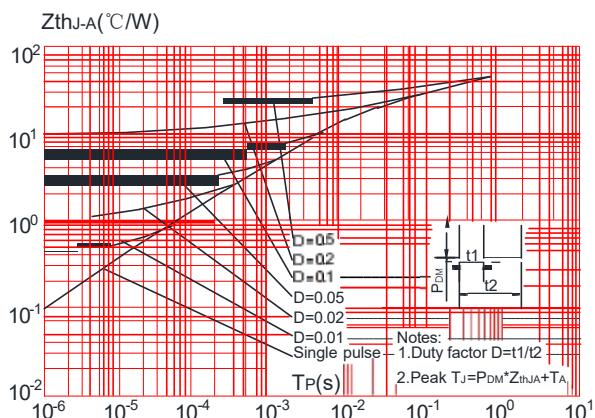


Figure 8: Normalized on Resistance vs. Junction Temperature

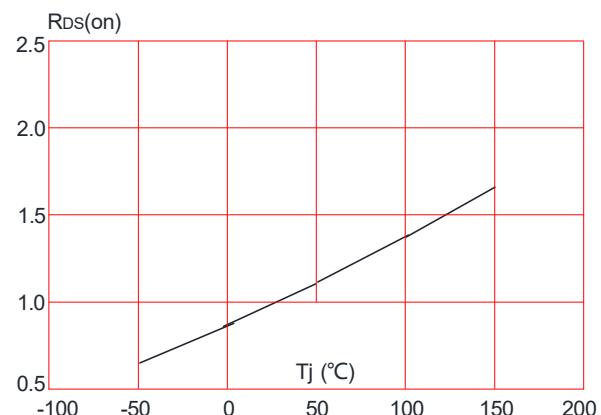
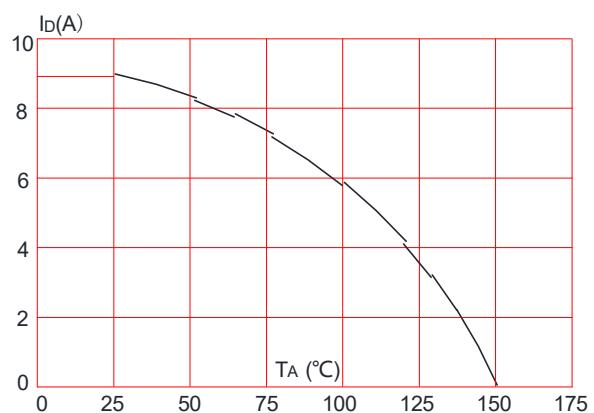
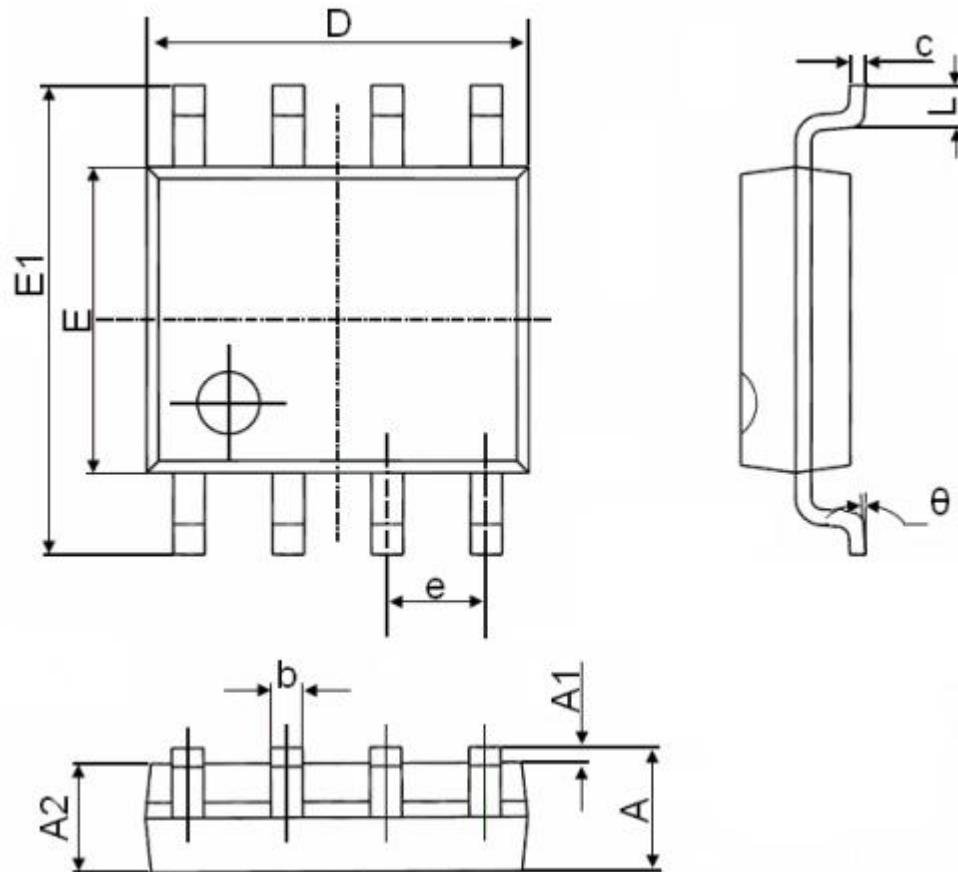


Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature



Package Mechanical Data- SOP-8



| Symbol | Dimensions In Millimeters | | Dimensions In Inches | |
|--------|---------------------------|-------|----------------------|-------|
| | Min. | Max. | Min. | Max. |
| A | 1.350 | 1.750 | 0.053 | 0.069 |
| A1 | 0.100 | 0.250 | 0.004 | 0.010 |
| A2 | 1.350 | 1.550 | 0.053 | 0.061 |
| b | 0.330 | 0.510 | 0.013 | 0.020 |
| c | 0.170 | 0.250 | 0.006 | 0.010 |
| D | 4.700 | 5.100 | 0.185 | 0.200 |
| E | 3.800 | 4.000 | 0.150 | 0.157 |
| E1 | 5.800 | 6.200 | 0.228 | 0.244 |
| e | 1.270(BSC) | | 0.050(BSC) | |
| L | 0.400 | 1.270 | 0.016 | 0.050 |
| theta | 0. | 8. | 0. | 8. |